	Spec No.	TQ3C-8EAF0-E1YAL84-02
SPEC for Mass Production	Date	March 10, 2025

TYPE : TCG121XGLPBPNN-AN40-TA
<12.1 inch XGA transmissive color TFT with LED backlight
and constant current circuit for LED backlight>

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Issued
Mar. 11, 2025
KYOCERA
Corp.

KYOCERA CORPORATION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
April 5, 2022	*Y. Yamazaki*	*T. Fukui*	*I. Kawajiri*	*T. Sawada*

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Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.

2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

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Revision record

Date	Designed by : Engineering dept.			Confirmed by : QA dept.
	Prepared	Checked	Approved	Approved
March 10, 2025	*Y. Yamazaki*	*T. Fukui*	*I. Kawajiri*	*T. Sawada*

Rev. No.	Date	Page	Descriptions
01	Jan. 25, 2024	5	1) V_{IN}-turn-on conditions Modify the diagram.
		11	7-3. Data mapping (6bit input) Modify the diagram.
02	Mar. 10, 2025	-	Remove the CONFIDENTIAL notation.
		2	4-2. Environmental absolute maximum ratings Revise the description of operating temperature.

		2-8	Changed the symbols.
			  Temp. → Ta
			  Ta → Temp.

		3, 4	5-1. LCD -Revise the maximum values of input leak current and LVDS input voltage. -Revise the V_{CM} value in Note 5).
		9	7-1. LCD -Add "(CN1)" to LCD connector and Matching connector. -Add the LCD connector (CN1).
		10	7-2. LED Add "(CN3)" to LCD connector and matching connector.
		17	9. Lot number identification Add version number.
		-	Revise the outline drawing.

1. Application

This document defines the specification of TCG121XGLPBPNN-AN40-TA.　(RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Anti-Glare treatment
Interface	: LVDS
Additional circuit	: Timing controller, Power supply (3.3V input)
	With constant current circuit for LED Backlight(12V input)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions　1)	260.5(W)×203(H)×10.4(D)	mm
Active area	245.76(W)×184.32(H) (30.8cm/12.1 inch(Diagonal))	mm
Dot format	1,024×(B,G,R)(W)×768(H)	dot
Dot pitch	0.08(W)×0.24(H)	mm
Base color　　　　2)	Normally Black	-
Mass	555	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage(+3.3V)		V_{DD}	-0.3	3.95	V
Supply voltage(+12V)		V_{IN}	-0.3	14.0	V
Input signal Voltage 1)	RxINi+, RxINi- (i=0,1,2,3)	V_{I1}	-0.3	V_{DD}+0.3	V
	CK IN+, CK IN-	V_{I2}	-0.3	V_{DD}+0.3	V
	MODE, SC	V_{I3}	-0.3	V_{DD}+0.3	V
	BLBRT, BLEN	V_{I4}	-0.3	V_{IN}	V

    1) V_{DD} must be supplied correctly within the range described in 5-1.

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1) 2)	T_{OP}	-30	80	°C
Storage temperature	3)	T_{STO}	-30	80	°C
Operating humidity	4)	H_{OP}	10	5)	%RH
Storage humidity	4)	H_{STO}	10	5)	%RH
Vibration		-	6)	6)	-
Shock		-	7)	7)	-

1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

2) Panel surface temperature (all the surface).

3) Ta (Ambient Temperature) = -30°C＜48h , Ta = 80°C＜168h
    Store LCD at normal temperature/humidity.　Keep them free from vibration and shock.
    An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.
    (Please refer to "Precautions for Use" for details.)

4) Non-condensing

5) Ta ≦40°C, 85%RH Max.
    Ta ＞40°C, Absolute humidity shall be less than 85%RH at 40°C.

6)

Frequency	10～55 Hz	Acceleration value $(0.3\sim9 \text{ m/s}^2)$
Vibration width	0.15mm	
Interval	10-55-10 Hz 1minute	

    2 hours in each direction X, Y, Z (6 hours total)
    EIAJ ED-2531

7) Acceleration: 490 m/s^2, Pulse width: 11 ms
   3 times in each direction: ±X, ±Y, ±Z
    EIAJ ED-2531

5. Electrical characteristics

5-1. LCD

Ta = -30～80°C

| Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	1)	V_{DD}	-	3.0	3.3	3.6	V
Current consumption		I_{DD}	2)	-	300	390	mA
Permissive input ripple voltage		V_{RP}	V_{DD}=3.3V	-	-	100	mVp-p
Input signal voltage	3)	V_{IL}	"Low" level	0	-	$0.3V_{DD}$	V
		V_{IH}	"High" level	$0.7V_{DD}$	-	V_{DD}	V
Input leak current		I_{OL}	V_{I3}=0V	-10	-	10	μ A
		I_{OH}	V_{I3}=3.3V	-	-	20	μ A
LVDS Input voltage	4)	V_L	-	0	-	2.1	V
Differential input voltage	4)	V_{ID}	-	200	-	600	mV
Differential input threshold voltage	4) 5)	V_{TL}	"Low" level	V_{CM}-100	-	-	mV
		V_{TH}	"High" level	-	-	V_{CM}+100	mV
Terminator		R_1	-	-	100	-	Ω
V_{DD}-turn-on conditions	1) 6)	t1	-	0.1	-	20	ms
		t2	-	10	-	-	ms
		t3	-	0	-	-	ms
		t4	-	2	-	-	s
		t5	-	200	-	-	ms
		t6	-	200	-	-	ms

1) V_{DD}-turn-on conditions
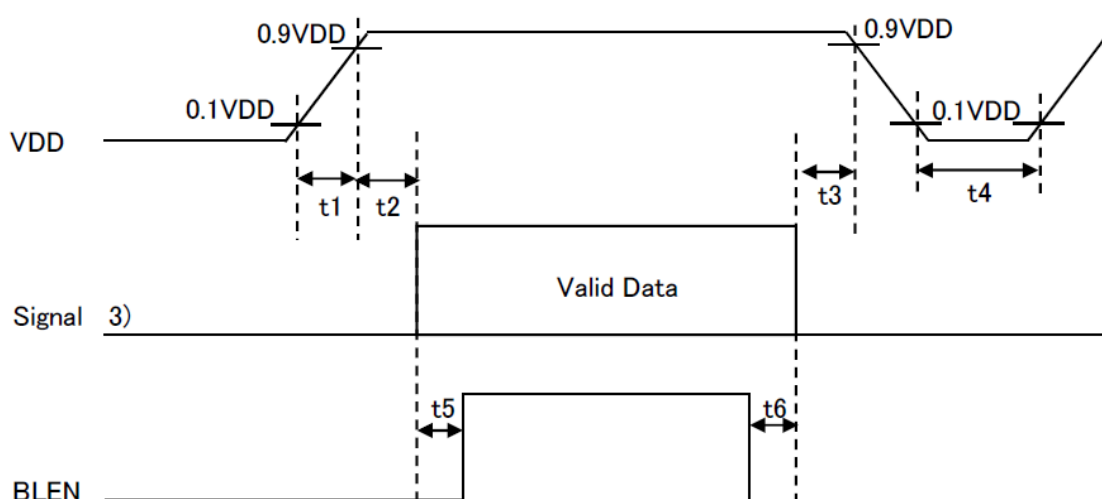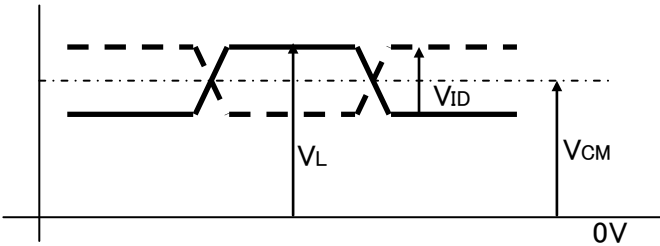
2) Display pattern:

V_{DD} = 3.3V, Ta = 25°C
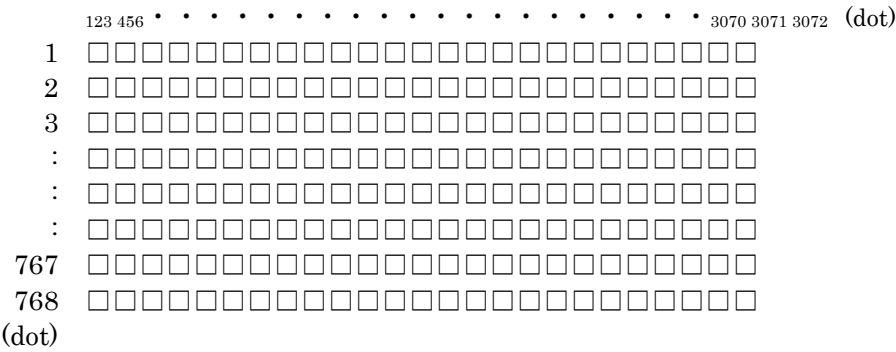
3) Input signal : MODE, SC

4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-
CK IN+, CK IN-

5) V_{CM} : LVDS Common mode voltage (V_{CM}=1.2V)

6) Please power on LVDS transmitter at the same time as VDD, or LVDS transmitter should be powered on first.

## 5-2. Constant current circuit for LED Backlight

Ta= -30～80°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage　　　　　　　　1)	V_{IN}	-	10.8	12.0	13.2	V
Current consumption	I_{IN}	2)	-	900	1,110	mA
Permissive input ripple voltage	V_{RP_BL}	V_{IN}=12.0V	-	-	100	mVp-p
BLBRT Input signal voltage	V_{IL_BLBRT}	"Low" level	0	-	0.8	V
	V_{IH_BLBRT}	"High" level	2.3	-	V_{IN}	V
BLBRT Input pull-down resistance	R_{IN_BLBRT}	-	100	300	500	kΩ
BLEN Input signal voltage	V_{IL_BLEN}	"Low" level	0	-	0.8	V
	V_{IH_BLEN}	"High" level	2.3	-	V_{IN}	V
BLEN Input pull-down resistance	R_{IN_BLEN}	-	100	300	500	kΩ
PWM Frequency　　　　　　　3)	f_{PWM}	-	200	-	10k	Hz
PWM Duty ratio　　　　　　　3)	D_{PWM}	f_{PWM}=200Hz	1	-	100	%
		f_{PWM}=2kHz	10	-	100	%
		f_{PWM}=10kHz	50	-	100	%
Operating life time　　　4), 5)	T	Temp.=25°C	-	70,000	-	h

1) V_{IN}-turn-on conditions
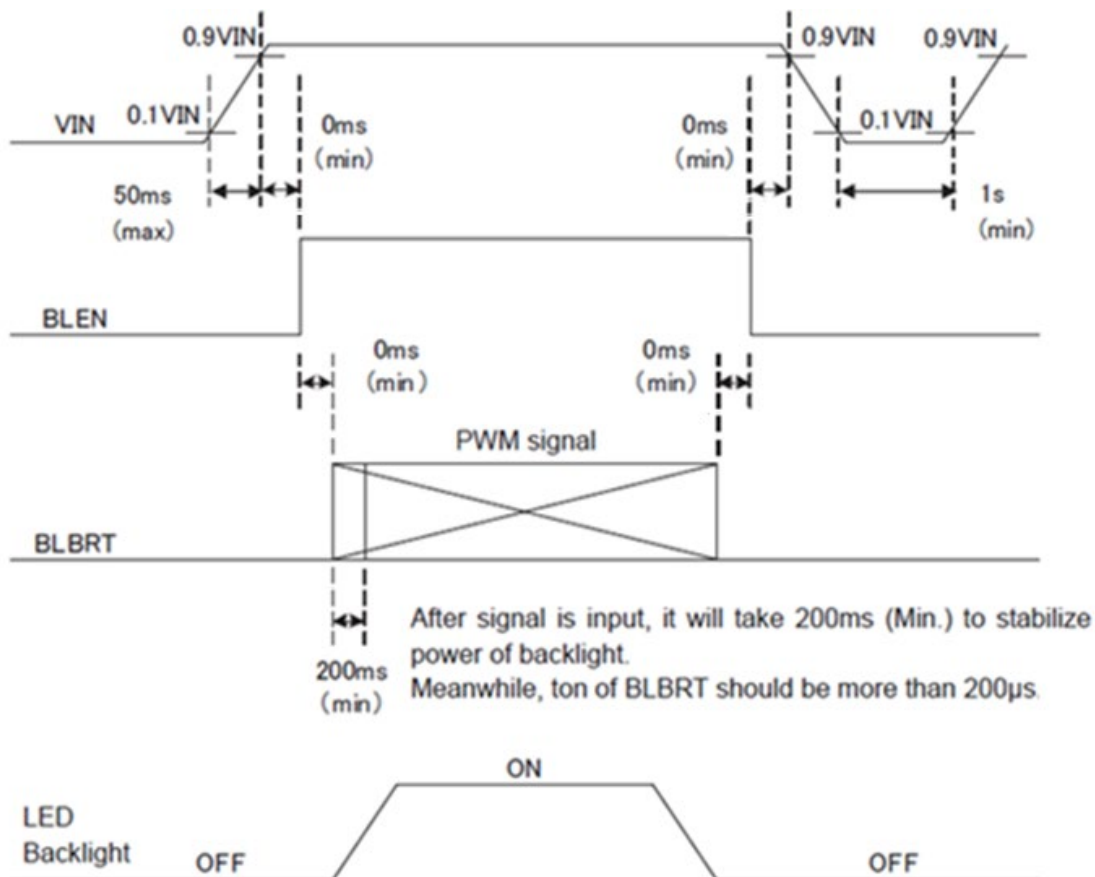
2) V_{IN} = 12V, Ta = 25℃, D_{PWM} = 100%

3) PWM timing diagram
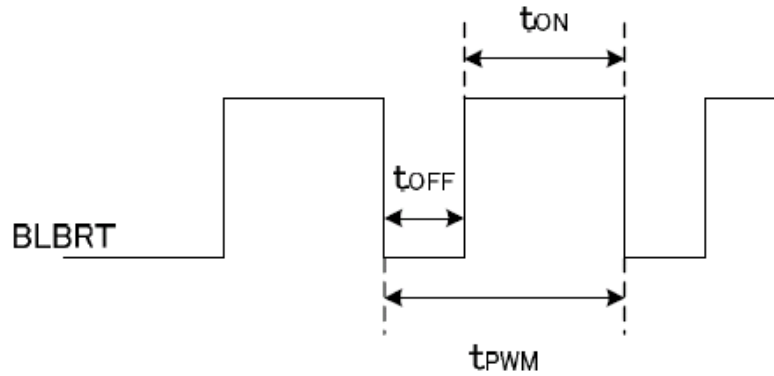
Please do not set t_{OFF} with $0 \ \mu s < t_{OFF} < 1 \ \mu s$.
In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

4) When brightness decrease 50% of minimum brightness.
The average life of a LED will decrease when the LCD is operating at higher temperatures.

5) Life time is estimated data. (Condition: $D_{PWM} = 100\%$, Temp. $=25℃$ in chamber).

6. Optical characteristics

Measuring spot = ϕ 6.0mm, Ta = 25°C

| Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_r	$\theta=\phi=0°$	-	18	-	ms
	Down	τ_d	$\theta=\phi=0°$	-	12	-	ms
Viewing angle range View direction	θ UPPER		CR$\geqq$10	-	85	-	deg.
	θ LOWER			-	85	-	
	ϕ LEFT			-	85	-	deg.
	ϕ RIGHT			-	85	-	
Contrast ratio		CR	$\theta=\phi=0°$	500	750	-	-
Brightness		L	D_{PWM} = 100%	840	1,200	-	cd/m²
Chromaticity coordinates	Red	x	$\theta=\phi=0°$	0.550	0.600	0.650	-
		y		0.300	0.350	0.400	
	Green	x	$\theta=\phi=0°$	0.285	0.335	0.385	
		y		0.520	0.570	0.620	
	Blue	x	$\theta=\phi=0°$	0.100	0.150	0.200	
		y		0.070	0.120	0.170	
	White	x	$\theta=\phi=0°$	0.270	0.320	0.370	
		y		0.295	0.345	0.395	

6-1. Definition of contrast ratio

$$CR(\text{Contrast ratio}) = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$
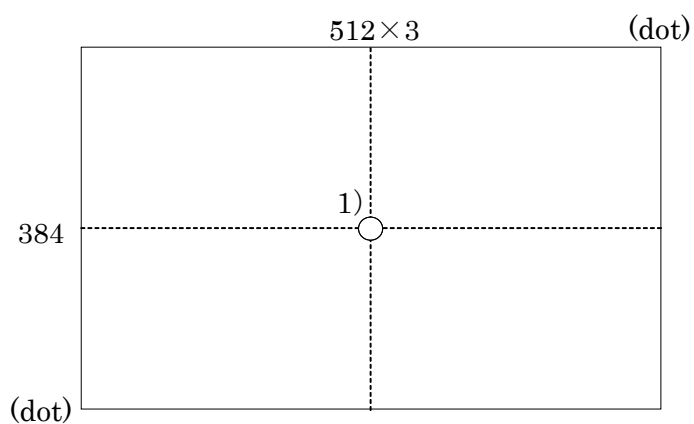
6-2. Definition of response time
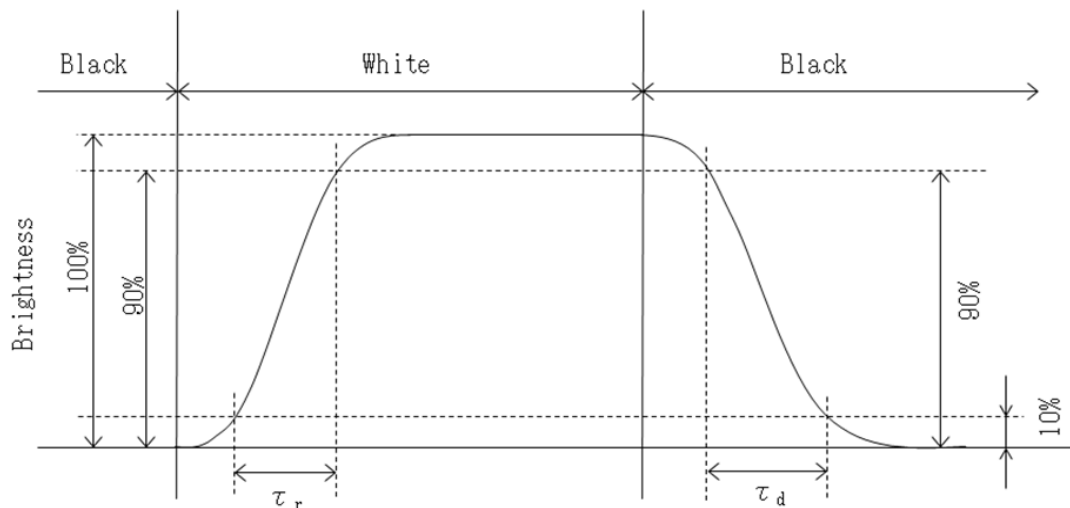
6-3. Definition of viewing angle

$\phi = 0^{\circ}$

θ direction

$\theta = 0^{\circ}$

ϕ direction
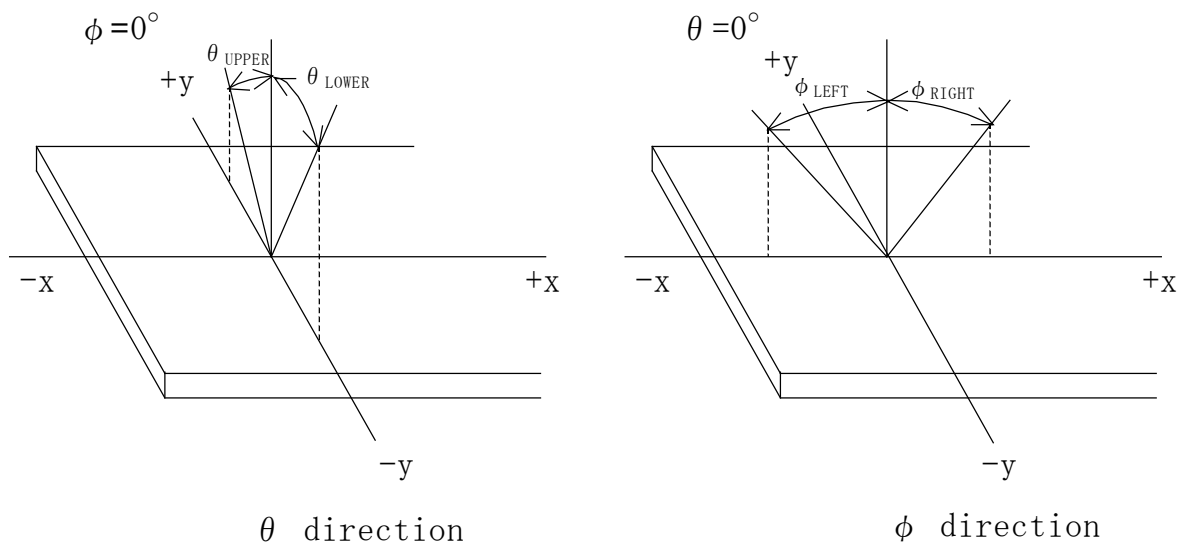
6-4. Brightness measuring point

512×3   (dot)

1)

384

(dot)

1) Rating is defined as the white brightness at center of display screen.
2) 5 minutes after LED is turned on. (Ta =25°C)

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7. Interface signals

7-1. LCD

No.	Symbol	Description	Note
1	V$_{DD}$	+3.3V power supply	
2	V$_{DD}$	+3.3V power supply	
3	GND	GND	
4	GND	GND	
5	RxIN0-	LVDS receiver signal CH0(-)	LVDS
6	RxIN0+	LVDS receiver signal CH0(+)	LVDS
7	GND	GND	
8	RxIN1-	LVDS receiver signal CH1(-)	LVDS
9	RxIN1+	LVDS receiver signal CH1(+)	LVDS
10	GND	GND	
11	RxIN2-	LVDS receiver signal CH2(-)	LVDS
12	RxIN2+	LVDS receiver signal CH2(+)	LVDS
13	GND	GND	
14	CK IN1-	LVDS receiver signal CK(-)	LVDS
15	CK IN1+	LVDS receiver signal CK(+)	LVDS
16	GND	GND	
17	RxIN3-	LVDS receiver signal CH3(-)	LVDS
18	RxIN3+	LVDS receiver signal CH3(+)	LVDS
19	MODE	Bit data select signal(GND: 6bit mode、 High: 8bit mode)	
20	SC	Scan direction control	1)

LCD connector (CN1)   : 20186-020E-11F (I-PEX)

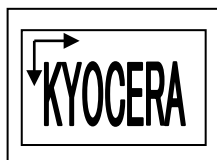   FI-SEB20P-HFE (JAE)

Matching connector (CN1) : FI-S20S   (JAE)

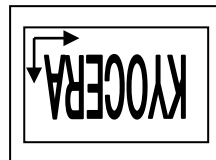LVDS receiver   : Embedded in ASIC

Matching LVDS transmitter : THC63LVDM83D (THine Electronics) or compatible

1) Scan direction

  SC：GND   SC：High

7-2. LED

No.	Symbol	Description	Note
1	V$_{IN}$	+12V power supply	
2	V$_{IN}$	+12V power supply	
3	BLBRT	PWM signal(Brightness adjustment)	
4	BLEN	ON/OFF terminal voltage	
5	GND	GND	
6	GND	GND	

LED connector (CN3)   :   SM06B-SHLS-G-TF(LF)(SN)   (JST)

Matching connector (CN3)   :   SHLP-6V-S-B   (JST)
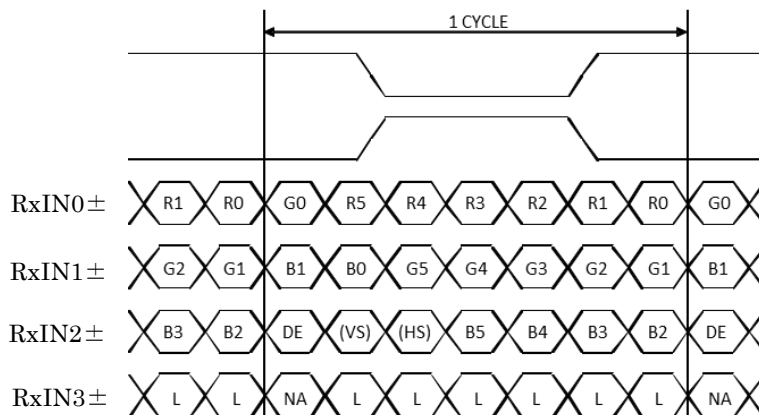
7-3. Data mapping (6bit input)

1) Location of MODE (THC63LVDM83D (THine Electronics) or compatible)

Transmitter		MODE
Pin No.	Data	= L(GND)
51	TA0	R0(LSB)
52	TA1	R1
54	TA2	R2
55	TA3	R3
56	TA4	R4
3	TA5	R5(MSB)
4	TA6	G0(LSB)
6	TB0	G1
7	TB1	G2
11	TB2	G3
12	TB3	G4
14	TB4	G5(MSB)
15	TB5	B0(LSB)
19	TB6	B1
20	TC0	B2
22	TC1	B3
23	TC2	B4
24	TC3	B5(MSB)
27	TC4	(HS)
28	TC5	(VS)
30	TC6	DE
50	TD0	GND
2	TD1	GND
8	TD2	GND
10	TD3	GND
16	TD4	GND
18	TD5	GND
25	TD6	(NA)

MODE=L (GND)

DE : DATA ENABLE

HS : H$_{SYNC}$

VS : V$_{SYNC}$

![KYOCERA logo]
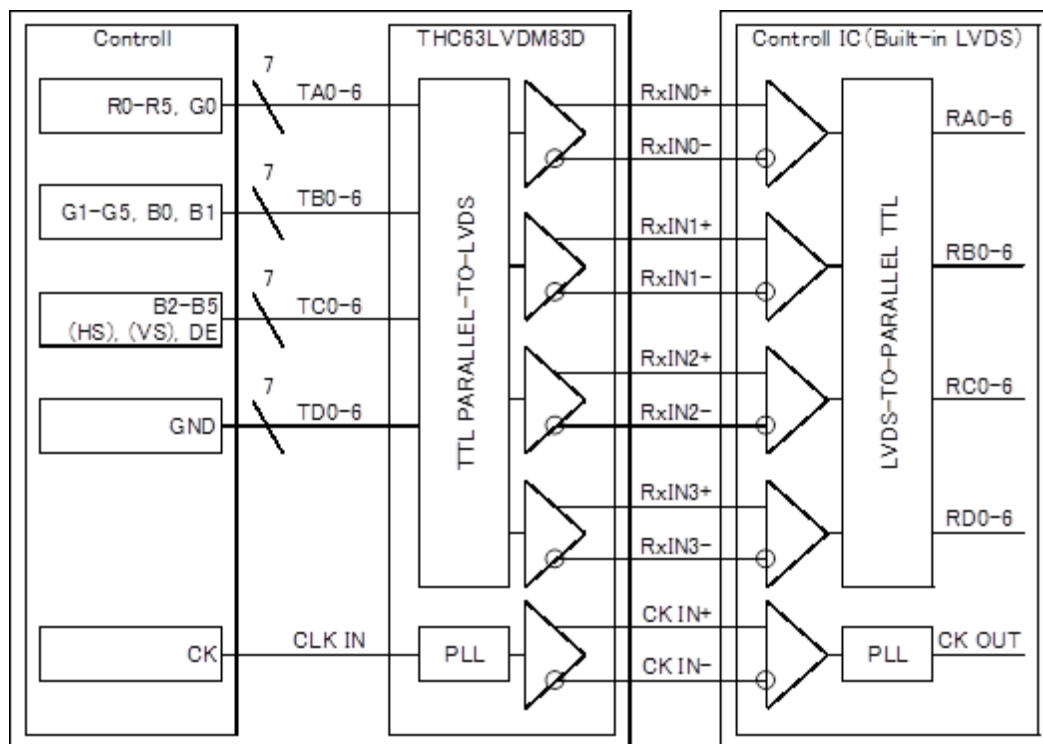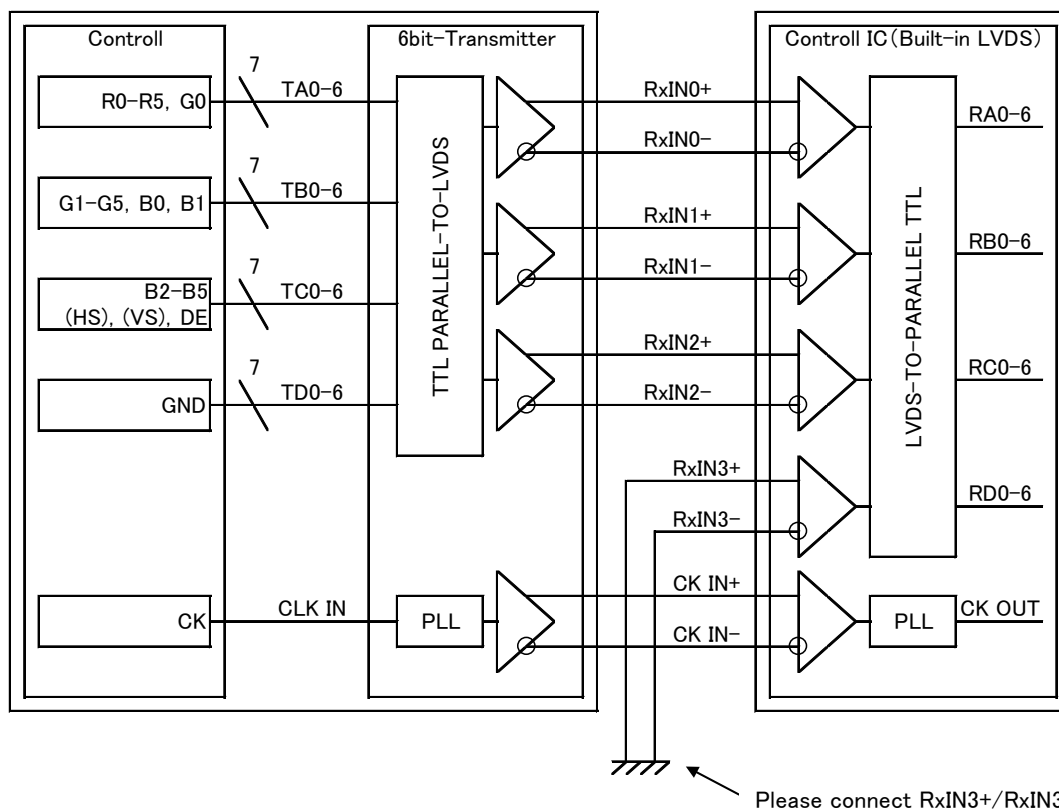
2) Block diagram

MODE=L (GND)

When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.


Please connect RxIN3+/RxIN3- to GND.

7-4. Data mapping (8bit input)

1) Location of MODE (THC63LVDM83D (THine Electronics) or compatible)

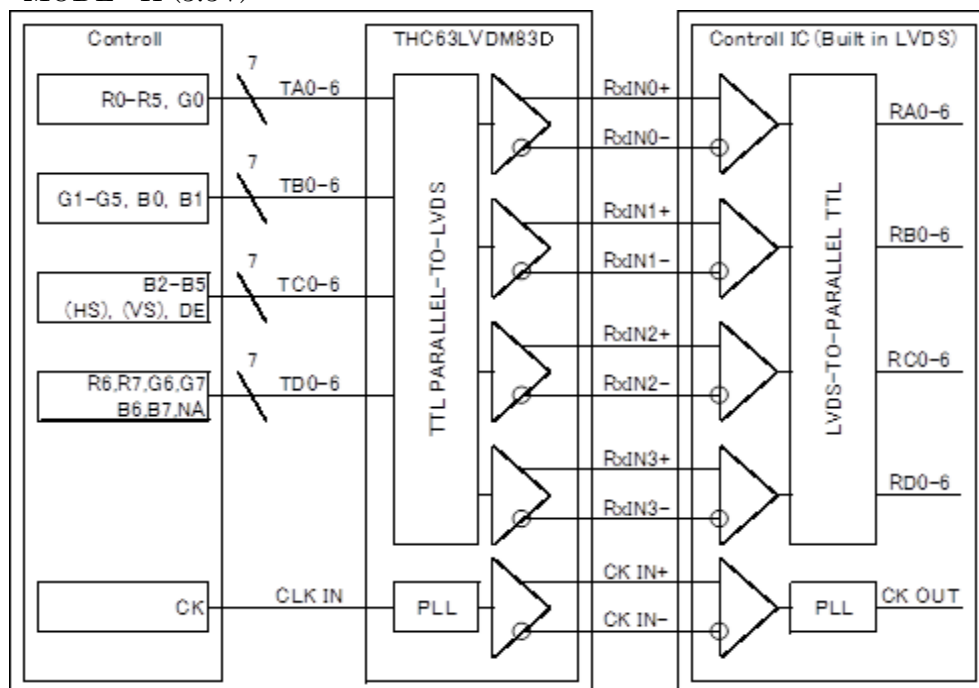| Transmitter		MODE
Pin No.	Data	= H(3.3V)
51	TA0	R0(LSB)
52	TA1	R1
54	TA2	R2
55	TA3	R3
56	TA4	R4
3	TA5	R5
4	TA6	G0(LSB)
6	TB0	G1
7	TB1	G2
11	TB2	G3
12	TB3	G4
14	TB4	G5
15	TB5	B0(LSB)
19	TB6	B1
20	TC0	B2
22	TC1	B3
23	TC2	B4
24	TC3	B5
27	TC4	(HS)
28	TC5	(VS)
30	TC6	DE
50	TD0	R6
2	TD1	R7(MSB)
8	TD2	G6
10	TD3	G7(MSB)
16	TD4	B6
18	TD5	B7(MSB)
25	TD6	(NA)

MODE= H(3.3V)
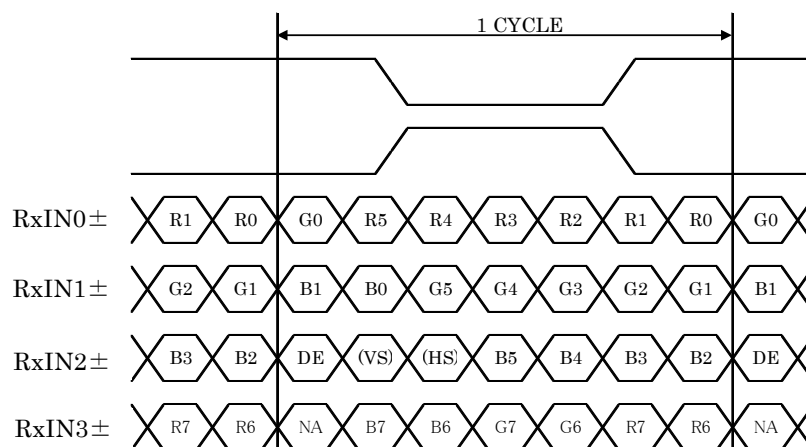
DE : DATA ENABLE

HS : H$_{SYNC}$

VS : V$_{SYNC}$

2) Block diagram

MODE= H (3.3V)

8. Input timing characteristics
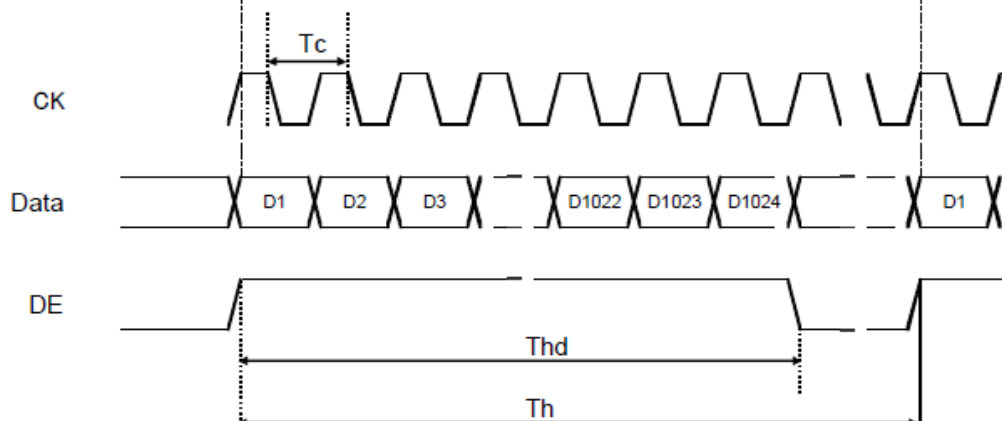
8-1. Timing characteristics

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	52	65	71	MHz	1)
Enable signal (DE)	Horizontal Period	Th	1,114	1,344	1,400	Dot	
			15.7	20.7	23.7	μs	2)
	Horizontal display period	Thd		1,024		Tc	
	Vertical Period	Tv	778	806	845	Line	
	Vertical display period	Tvd		768		Th	
Refresh rate		fv	50	60	82	Hz	3)

1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur. Please use the display under the conditions written in the specification.

2) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.

3) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur.(fv=1/Tv)
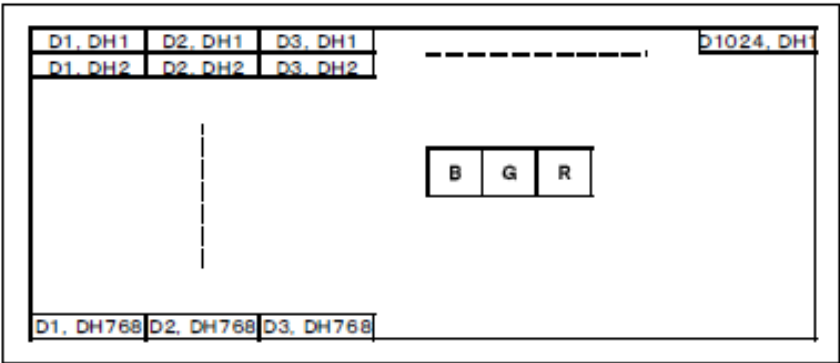
Vertical Timing Diagram
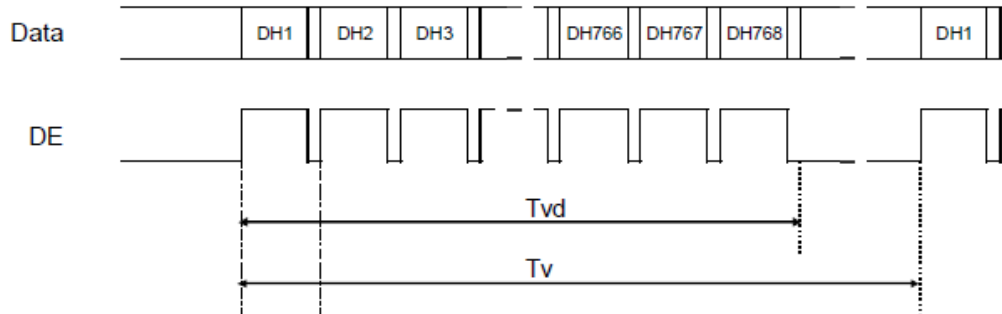
Horizontal Timing Diagram

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8-2. Input data signals and display position on the screen

9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

TCG121XGLPBPNN-AN40-TA☐☐☐☐☐:☐☐-☐☐☐☐☐☐☐☐ MADE IN ☐☐☐☐☐☐

①   ②   ③④   ⑤   ⑥   ⑦

No.① – No.⑦ above indicate
- ① Data matrix (For internal control purpose only)
- ② Version number (Max. 4 characters)
- ③ Year code (The last digit of the year)
- ④ Month code
- ⑤ Day code
- ⑥ Version number (Max. 7 characters)
- ⑦ Country of origin

④ Month code

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants the LCD for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD that is shown to be Kyocera's responsibility.

11. Precautions for use

11-1. Installation of the LCD
1) A transparent protection plate shall be added to protect the LCD and its polarizer.
2) The LCD shall be installed so that there is no pressure on the LSI chips.
3) Since this product is wide viewing product, occurrence level of in-plane unevenness by the external stress is different compared to current normal viewing product. So there is a possibility that in-plane unevenness will be occurred by over twist, strain giving by attaching to LCD, and over pressure to touch panel. Please be careful of stress when designing the housing.
4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

11-2. Static electricity
1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation
1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

11-4. Storage
1) The LCD shall be stored within the temperature and humidity limits specified.
   Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage
1) DO NOT store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
6) Do not disassemble LCD because it will result in damage.
7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

12. Reliability test data

Test item	Test condition	Test time	Judgement	
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	80°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

2) The LCD is tested in circumstances in which there is no condensation.

3) The reliability test is not an out-going inspection.

4) The result of the reliability test is for your reference purpose only.
     The reliability test is conducted only to examine the LCD's capability.

249 (Bezel Opening:ベゼル開口)

(7.1) 4.4

(1.62) 245.76 (Active Area:表示領域) 1.62

(1.59)

(8.35)

10.4

A

1024×(B,G,R)×768Dots

(102.1)

203

100.9

164.2

121.9

17.8

184.32 (Active Area:表示領域)

187.5 (Bezel Opening:ベゼル開口)

Bezel Opening Center
(ベゼル開口中心)

161

131.4

21

6-M3 USER HOLE
MIN DEPTH : 2.3mm
MAX. TORQUE 0.35N·m

4.5

4-R1

(131.6) 128.9

260.5

1.59
7.15

4.5
9.1

Note. (注記)

⚠ 1. Connector CN1： 20186-020E-11F (I-PEX)
   (コネクター)   FI-SEB20P-HFE (JAE)
      CN3： SM06B-SHLS-G-TF(LF)(SN) (JST)
      ⚠ ~~20197-020U-F (I-PEX)~~
      ⚠ ~~20197-T20U-F (I-PEX)~~

2. Matching Connector CN1： FI-S20S (JAE)
   (適合コネクター)   CN3： SHLP-6V-S-B (JST)

3. The Information of LCD is displayed starting at the upper left hand
   corner, moving right then down to the lower right hand corner.
   (LCDにおいて、画像データの表示は左上コーナーから始まり、右へ進み下へ送られ右下コーナーへ向かう。)

4. Tolerance without indication：±0.5
   (指示無き公差)

5. There is a possibility that in-plane unevenness will be occurred by over twist,
   stress giving by attaching to LCD, and over pressure to touch panel.
   Please be careful of stress when designing the housing.
   (過度な捻り(ひずみ)、LCD固定時での応力発生、タッチパネルでの過度な押し圧によっては、パネル面内にムラが発生することが
   ありますので、筐体設計において、事前に十分確認願います。)

6. Max screw length : 2.3mm
   (最大ネジ長)

7. Max screw torque : 0.35N·m
   (最大ネジ締め付けトルク)

DETAIL A (Dot Size)
(NTS)

B G R B G R
B G R B G

shield cover
（ シールドカバー ）
10.4mm thickness.
（厚み10.4mm）

▨ ： Projected part is 10.4mm thickness.
（厚み10.4mm）

(17.2)   (209.5)   (33.8)

(73.9)   (59)   (56.05)

No.1 No.6   No.1   No.20

CN3   CN1

Insert Direction   Insert Direction
（挿入方向）   （挿入方向）

(129.1)   (144)   (146.95)

Tape
（ テープ ）

Label ⚠   ⚠ Label
（ ラベル ）   （ ラベル ）

(120.6)   (74.6)   (31.5)   (33.8)
(174.1)   (86.4)
(145.7)   (114.8)

Note. (注記)
1. Tolerance without indication : ±0.5
（ 指示無き公差 ）

Material 材 質	Treatment 処 理	Approved '14.12.10 加藤	Checked	Checked '14.12.10 倉元	Drawn 圓福	Scale 1:1	Title TCG121XGLPBPNN	KYOCERA	Year—Month—Day '14.12.10	Size 2
Quantity 製作数	Description; 備 考 RoHS					⊕ ◁	Outline Dimensions	Drawing No. 121A8083200—3		2/2

KYOCERA INSPECTION STANDARD

TYPE : TCG121XGLPBPNN-AN40-TA

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
April 5, 2022	Y. Yamazaki	T. Fukui	I. Kawajiri	T. Sawada

Revision record

Date	Designed by : Engineering dept.			Confirmed by : QA dept.
	Prepared	Checked	Approved	Approved
March 10, 2025	*Y. Yamazaki*	*T. Fukui*	*I. Kawajiri*	*T. Sawada*

Rev. No.	Date	Page	Descriptions
01	Mar. 10, 2025	-	Remove the CONFIDENTIAL notation.
		2	Color variation (Mura) Modify the judgement standard.
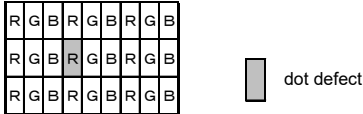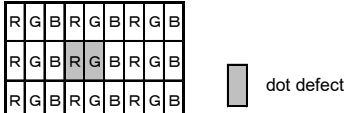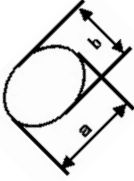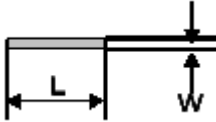
Visuals specification

1) Note

	Note
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area. 3. Inspection conditions 　Luminance　　　　　　　　　　: 500 Lux min. 　Inspection distance　　　　　: 300 mm. 　Temperature　　　　　　　　: 25 ± 5℃ 　Direction　　　　　　　　　: Directly above

Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the LCD, even when all "Black" data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter.
		Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen. Similar size compared to bright dot.
		White dot (Circular/foreign particle)	Pixel works electrically, however, circular/foreign particle makes dot appear to be "on" even when all "Black" data is sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixels "Black" or "White") and non operating.
		Appearance inspection	Does not satisfy the value at the spec.
	Definition of size	Definition of circle size　　　　　　　Definition of linear size a: major axis, b: minor axis d = (a + b) / 2	

2) Standard

Classification		Inspection item		Judgement standard		
Defect (in LCD glass)	Single dot	Bright dot defect		Acceptable number　　　　: 4		
				Bright dot spacing　　　　: 5 mm or more		
		Black dot defect		Acceptable number　　　　: 5		
				Black dot spacing　　　　: 5 mm or more		
	Adjacent dot	2 dots	Bright dot defect	Acceptable number　　　　: 2		
			Black dot defect	Acceptable number　　　　: 3		
		3 or more dots		Acceptable number　　　　: 0		
	Total dot defects			Acceptable number　　　　: 5 Max		
	Others	White dot, Dark dot (Circle)		Size (mm)		Acceptable number
				d ≦ 0.2		(Neglected)
				0.2 < d ≦ 0.4		5
				0.4 < d ≦ 0.5		3
				0.5 < d		0
External inspection (Defect on Polarizer or between Polarizer and LCD glass)		Polarizer (Scratch)		Width (mm)	Length (mm)	Acceptable number
				W ≦ 0.1	−	(Neglected)
				0.1 < W ≦ 0.3	L ≦ 5.0	(Neglected)
					5.0 < L	0
				0.3 < W	−	0
		Polarizer (Bubble)		Size (mm)		Acceptable number
				d ≦ 0.2		(Neglected)
				0.2 < d ≦ 0.3		5
				0.3 < d ≦ 0.5		3
				0.5 < d		0
		Foreign particle (Circular shape)		Size (mm)		Acceptable number
				d ≦ 0.2		(Neglected)
				0.2 < d ≦ 0.4		5
				0.4 < d ≦ 0.5		3
				0.5 < d		0
		Foreign particle (Linear shape) Scratch		Width (mm)	Length (mm)	Acceptable number
				W ≦ 0.03	−	(Neglected)
				0.03 < W ≦ 0.1	L ≦ 2.0	(Neglected)
					2.0 < L ≦ 4.0	3
					4.0 < L	0
				0.1 < W	−	(According to circular shape)
		Color variation (Mura)		Not visible through 5% ND filter.		

KYOCERA

KYOCERA PACKAGING STANDARD

TYPE : TCG121XGLPBPNN-AN40-TA

KYOCERA CORPORATION

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
April 5, 2022	Y. Yamazaki	T. Fukui	I. Kawajiri	T. Sawada

KYOCERA

Revision record

Date	Designed by : Engineering dept.				Confirmed by : QA dept.
	Prepared	Checked	Approved		Approved
March 10, 2025	Y. Yamazaki	T. Fukui	I. Kawajiri		T. Sawada

Rev. No.	Date	Page	Descriptions
01	Mar. 10, 2025	-	Remove the CONFIDENTIAL notation.

KYOCERA

TCG121XGLP (without Touch Panel) Packaging Standard	

1. How to store LCDs in the pad

Pad

Anti-static bag

Max. 1pc / pad

KYOCERA

TCG121XGLP (without Touch Panel) Packaging Standard	

2. How to store LCDs in the box

Partition board

Pad

Pad

Outer case

Pad

Mat

Pad

Pad

The pad which the products are stored

Max. 14pcs / Case

3. Location of the labels

PP band

Single-sided tape

360

320

540

Shipping label

Comfirmation label

MESSAGE	
P/O No.	
DESCRIPTION	
PARTS No. OR DWG No.	LOT No.
QUANTITY	DATE

KYOCERA CORPORATION

客先 (Customer)		京
品名 (Parts No.)		セ ラ
数量 (QTY)			株 式
検査者 (Check)			会
検査者 (Check)			社

KYOCERA