SPEC for Mass Production Date April 13, 2023 Date April 23, 2023 Colspan="2">Date April 23, 2023 Application Date Application 2 Construction and outline Mechanical specifications 3 Absolute maximum ratings Electrical characteristics 6 Optical characteristics Application 1 Meranty Backlight characteristics 10 Lot number identification Input timing characteristics 10 Lot number identification Input timing characteristics 10 Lot nu				Spec No.	TQ3C-8EAF0-E	1YBD81-00
 5.7 inch VGA transmissive color TFT with LED backlight> CONTENTS 1. Application 2. Construction and outline 3. Mechanical specifications 4. Absolute maximum ratings 5. Electrical characteristics 6. Optical characteristics 7. Interface signals 8. Input timing characteristics 9. Backlight characteristics 10. Lot number identification 11. Warranty 12. Precautions for use 13. Reliability test data 14. Outline drawing 	SPEC for M	lass Proc	duction	Date	April 13, 2	2023
 Application Construction and outline Mechanical specifications Absolute maximum ratings Electrical characteristics Optical characteristics Interface signals Input timing characteristics Backlight characteristics Lot number identification Warranty Precautions for use Reliability test data Outline drawing 				ve color '	ГГТ	
 2. Construction and outline 3. Mechanical specifications 4. Absolute maximum ratings 5. Electrical characteristics 6. Optical characteristics 7. Interface signals 8. Input timing characteristics 9. Backlight characteristics 10. Lot number identification 11. Warranty 12. Precautions for use 13. Reliability test data 14. Outline drawing 			CONTI	ENTS		
Apr.20, 2023 KYOCERA KYOCERA CORPORATION		 Construct Mechanics Absolute n Electrical Optical ch Interface n Input time Backlight Lot numb Warranty Precaution Reliability 	tion and outline al specifications maximum rating characteristics naracteristics signals ing characterist characteristics er identification ns for use y test data	gs ics	Apr.20 KYOC	, 2023 ERA
This specification is subject to change without notice. Consult Kyocera before ordering.					out notice.	
Original Designed by: Engineering dept. Confirmed by: QA dept.	Original	Designed by: E	ngineering dept	· · · · · · · · · · · · · · · · · · ·	Confirmed by	QA dept.
Laura Data		Prepared	Checked	Approved	Checked	Approved
April 13, 2023 K. Komurasaki I. Kawajiri A. Iwasaki Y. Aritsubo M. Aoyam	April 13, 2023	K. Komurasaki	I. Kawajiri	A. Iwasak.	i Y. Aritsubo	M. Aoyama





	Page	
TQ3C-8EAF0-E1YBD81-00 TCG057VGLDFANN-GN00	-	

Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

			Spec No.		Part No.		Pa
			TQ3C-8EAF0-E	1YBD81-00	TCG057VGLDF		
		R	evision 1				
Τ			v: Engineering		Confirmed by	v∶QA dept.	
L	Date	Prepared	Checked	Approved		Approved	
Rev.No.	Date	Page		Descrip	tions		

Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	1

1. Application

This document defines the specification of TCG057VGLDFANN-GN00. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input)
	(without constant current circuit for LED Backlight)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	134.5(W)×103.4(H)×8(D)	mm
Active area	115.2(W)×86.4(H) (14.4cm/5.7 inch(Diagonal))	mm
Dot format	640×(B,G,R)(W)×480(H)	dot
Dot pitch	0.06(W)×0.18(H)	mm
Base color 2)	Normally White	-
Mass	145	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage		V_{DD}	0	4.0	V
Input signal voltage	1)	$V_{\rm IN}$	-0.3	6.0	V
LED forward current	2)	IF	-	100	mA

- 1) Input signal : CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D
- 2) For each "AN-CA"
- 3) Do not apply reversed voltage.
- 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	TOP	-20	70	°C
Storage temperature	2)	Тѕто	-30	80	°C
Operating humidity	3)	Hop	10	4)	%RH
Storage humidity	3)	$\mathrm{H}_{\mathrm{STO}}$	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Temp. = -30°C \leq 48h , Temp. = 80°C \leq 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)

- 3) Non-condensing
- 4) Temp.≦40°C, 85%RH Max.
 - Temp.>40°C, Absolute humidity shall be less than 85%RH at 40°C.
- 5)

Frequency	$10{\sim}55~{\rm Hz}$	Acceleration value
Vibration width	0.15mm	$(0.3 \sim 9 \text{ m/s}^2)$
Interval	10-55-1	0 Hz 1minute

2 hours in each direction X, Y, Z (6 hours total) EIAJ ED-2531

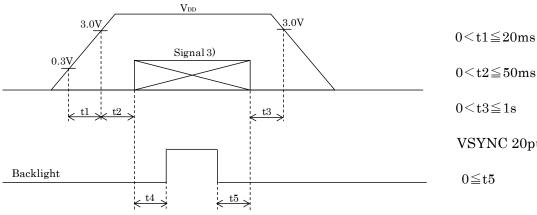
 6) Acceleration: 490 m/s², Pulse width: 11 ms 3 times in each direction: ±X, ±Y, ±Z EIAJ ED-2531

Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	3

5. Electrical characteristics

		-			Temp. = -2	$0\sim 70^{\circ}\mathrm{C}$
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage 1)	V_{DD}	-	3.0	3.3	3.6	V
Current consumption	Idd	2)	-	150	195	mA
Permissive input ripple voltage	V_{RP}	-	-	-	100	mVp-p
	VIL	"Low" level	0	-	$0.3 V_{DD}$	V
Input signal voltage 3)	VIH	"High" level	$0.7 V_{DD}$	-	V_{DD}	V

1) V_{DD}-turn-on conditions



 $0 \le t1 \le 20 ms$

 $0\!<\!t3\!\leq\!1s$

VSYNC 20pulse≦t4

 $0 \leq t5$

2) Display pattern: $V_{DD} = 3.3V$, Temp. = 25°C • • 1918 1919 1920(dot) 123 456 • 1 $\mathbf{2}$ 3 : : : 479(dot)

3) Input signal : CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	4

6. Optical characteristics

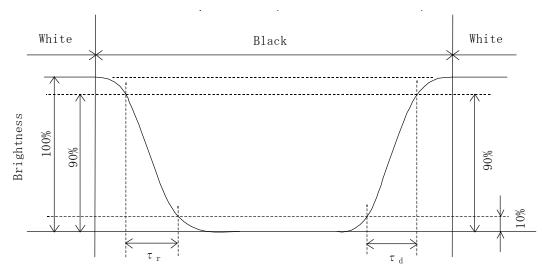
Measuring spot = ϕ 6.0mm, Temp. = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
	Rise	τr	$\theta = \phi = 0^{\circ}$	-	10	-	ms	
Response time	Down	τd	$\theta = \phi = 0^{\circ}$	-	25	-	ms	
		heta upper		-	80	-	1	
Viewing angle View direction	range	θ lower		-	80	-	deg.	
÷ 12 o'clo		ϕ left	$CR \ge 5$	-	80	-	1	
(Gray in	version)	ϕ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	300	500	-	-	
Brightness		L	IF=60mA/Line	280	400	-	cd/m^2	
	D 1	х	$\theta = \phi = 0^{\circ}$	0.56	0.61	0.66		
	Red	У		0.31	0.36	0.41		
	0	х	$\theta = \phi = 0^{\circ}$	0.30	0.35	0.40		
Chromaticity	Green	У	$\theta = \phi = 0^{-1}$	0.52	0.57	0.62		
coordinates	DI	х	0 - 1 - 08	0.09	0.14	0.19	-	
	Blue	У	$\theta = \phi = 0^{\circ}$	0.06	0.11	0.16		
	XX71 , 14 -	х	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
	White	у	$\sigma - \phi - 0^{2}$	0.30	0.35	0.40		

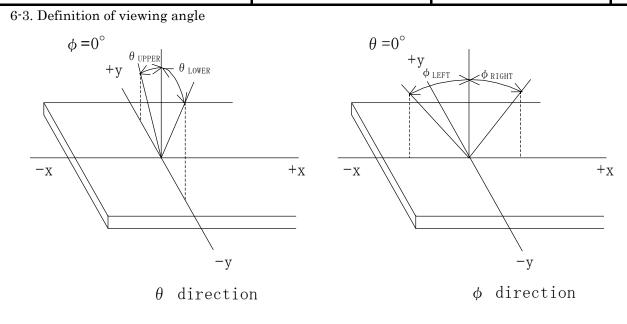
6-1. Definition of contrast ratio

CR(Contrast ratio) = Brightness with all pixels "White" Brightness with all pixels "Black"

6-2. Definition of response time

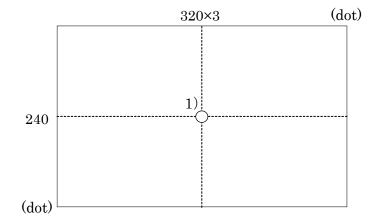


Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	5



6-4. Brightness measuring point





- 1) Rating is defined as the white brightness at center of display screen.
- 2) Measured 5 minutes after LED is turned on. (Ambient Temp.=25°C)



7. Interface signals

7-1. LCD

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	СК	Clock signal for sampling each data signal	Ι	
3	H _{SYNC}	Horizontal synchronous signal (negative)	Ι	
4	V _{SYNC}	Vertical synchronous signal (negative)	Ι	
5	GND	GND	-	
6	R0	RED data signal (LSB)	Ι	
7	R1	RED data signal	Ι	
8	R2	RED data signal	Ι	
9	R3	RED data signal	Ι	
10	R4	RED data signal	Ι	
11	R5	RED data signal (MSB)	Ι	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	Ι	
14	G1	GREEN data signal	Ι	
15	G2	GREEN data signal	Ι	
16	G3	GREEN data signal	Ι	
17	G4	GREEN data signal	Ι	
18	G5	GREEN data signal (MSB)	Ι	
19	GND	GND	-	
20	B0	BLUE data signal (LSB)	Ι	
21	B1	BLUE data signal	Ι	
22	B2	BLUE data signal	Ι	
23	B3	BLUE data signal	Ι	
24	B4	BLUE data signal	Ι	
25	B5	BLUE data signal (MSB)	Ι	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	Ι	1)
28	V _{DD}	3.3V power supply	-	
29	V _{DD}	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L : Normal , H : Left / Right reverse mode	Ι	2)
31	U/D	Vertical display mode select signal H : Normal , L : Up / Down reverse mode	Ι	2)
32	NC	No connect	Ι	
33	GND	GND	-	

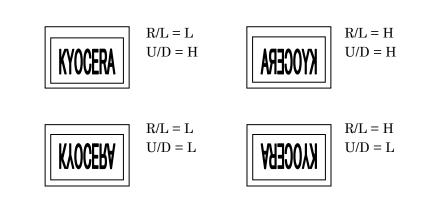
LCD connector Recommended matching FFC or FPC : 0.5mm pitch

: IMSA-9637S-33A-GFN4 (IRISO)



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	7

The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. 1) In case ENAB is fixed "Low", the horizontal start timing is determined. Don't keep ENAB "High" during operation.



7-2. LED

2)

No.	Symbol	Description
1	AN1	Anode1
2	AN2	Anode2
3	CA1	Cathode1
4	CA2	Cathode2

LCD side connector

: PHR-4

Recommended matching connector

: B4B-PH-SM4-TB

- : B4B-PH-SM4-TB(LF)(SN)
- : S4B-PH-SM4-TB
- : S4B-PH-SM4-TB(LF)(SN)

(JST)

- (JST)
- (JST) ···(RoHS Compliant) (JST)
- (JST) ···(RoHS Compliant)



8. Input timing characteristics

	Item	Symbol	Min	Тур	Max	Unit	Note
Clock	Frequency	1/Tc	22.66	25.18	27.69	MHz	2)
Clock	Duty ratio	Tch/Tc	40	50	60	%	
Data	Set up time	Tds	5	_	_	ns	
Data	Hold time	Tdh	10		— ns		
Horizontal sync. signal	Creale	711	30.0	31.8	_	μ s	
	Cycle	TH	770	800	850	clock	
	Pulse width	THp	2	96	200	clock	
Vertical sync.	Cycle	TV	515	525	560	line	
signal	Pulse width	TVp	2	—	34	line	
Horizontal displa	y period	THd	640			clock	
Hsync,-Clock phase difference		THc	10	—	Tc-10	ns	
Hsync-Vsync. phase difference		TVh	2Tc –		TH-THp-Tc	ns	
Vertical sync. signal start position		TVs	34			line	
Vertical display p	period	TVd		480		line	

8-1. Timing characteristics 1)

- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur. Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

8-2. Horizontal display position

Item		Symbol	Min	Тур	Max	Unit	Note
Enchle sime al	Set up time	Tes	5	_	Tc-10	ns	
Enable signal	Pulse width	Tep	2	640	TH-10	clock	
H _{SYNC} – Enable signal phase difference		The	44		104	clock	

- 1) When ENAB is fixed at "Low", the display starts from the data of C104(clock) as shown in 8-5.
- 2) The horizontal display position is determined by ENAB signal.

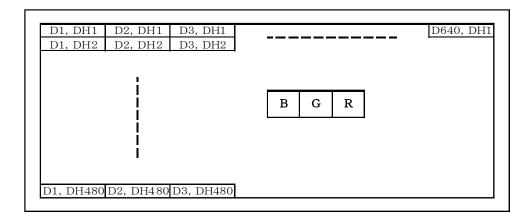
8-3. Vertical display position

- 1) The vertical display position (TVs) is 34th line.
- 2) ENAB signal is independent of vertical display position.

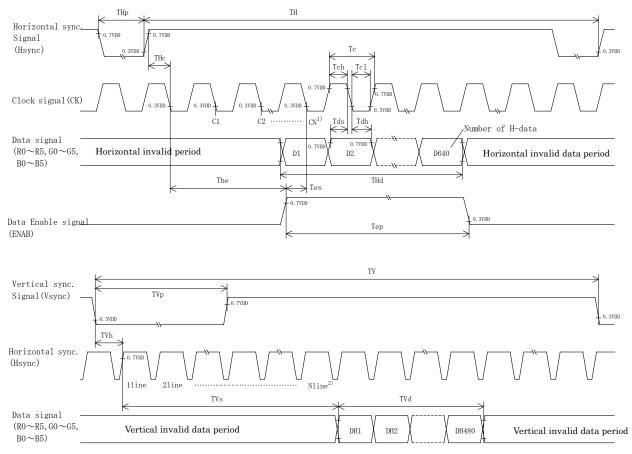


Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	9

8-4. Input data signals and display position on the screen



8-5. Input timing characteristics



- 1) When ENAB is fixed at "Low", the display starts from the data of C104 (Clock).
- 2) The vertical display position (TVs) is fixed at 34th line.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	10

9. Backlight characteristics

Item		Symbol	Min.	Тур.	Max.	Unit	Note
Forward current	1)	IF	-	60	-	mA	Ta=-20~70°C
			-	9.5	11.2	V	IF=60mA, Ta=-20°C
Forward voltage	1)	VF	-	9.0	10.6	V	IF=60mA, Ta=25°C
			-	8.7	10.3	V	IF=60mA, Ta=70°C
Operating life time	2), 3)	Т	-	100,000	-	h	IF=60mA, Ta=25°C

1) For each "AN-CA"

When brightness decrease 50% of minimum brightness.
 The average life of a LED will decrease when the LCD is operating at higher temperatures.

- 3) Life time is estimated data. (Condition : IF=60mA, Ta= 25° C in chamber).
- 4) An input current below 15mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 5) LED formation: 3 series, 2 parallel



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	11

10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.

TCG057VGLDFANN-G	N00:□□- <u>□</u>			MADE IN
\downarrow	$\downarrow \downarrow$	\downarrow	\downarrow	\downarrow
	23	4	5	6

- ① Data matrix (For internal control purpose only)
- 2 Year code (The last digit of the year)
- \bigcirc Month code
- ④ Day code
- (5) Version number (Max. 7 characters)
- 6 Country of origin

③ Month code

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	Х	Y	Z

11. Warranty

11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

11-2. Production warranty

Kyocera warrants the LCDs for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCDs that are shown to be Kyocera's responsibility.



12. Precautions for use

- 12-1. Installation of the LCD
- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) A transparent protection film is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not pull the LED lead wires and do not bend the root of the wires. Housing should be designed to protect LED lead wires from external stress.
- 7) Do not disassemble LCD because it will result in damage.
- 8) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 9) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 10) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YBD81-00	TCG057VGLDFANN-GN00	13

13. Reliability test data

Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

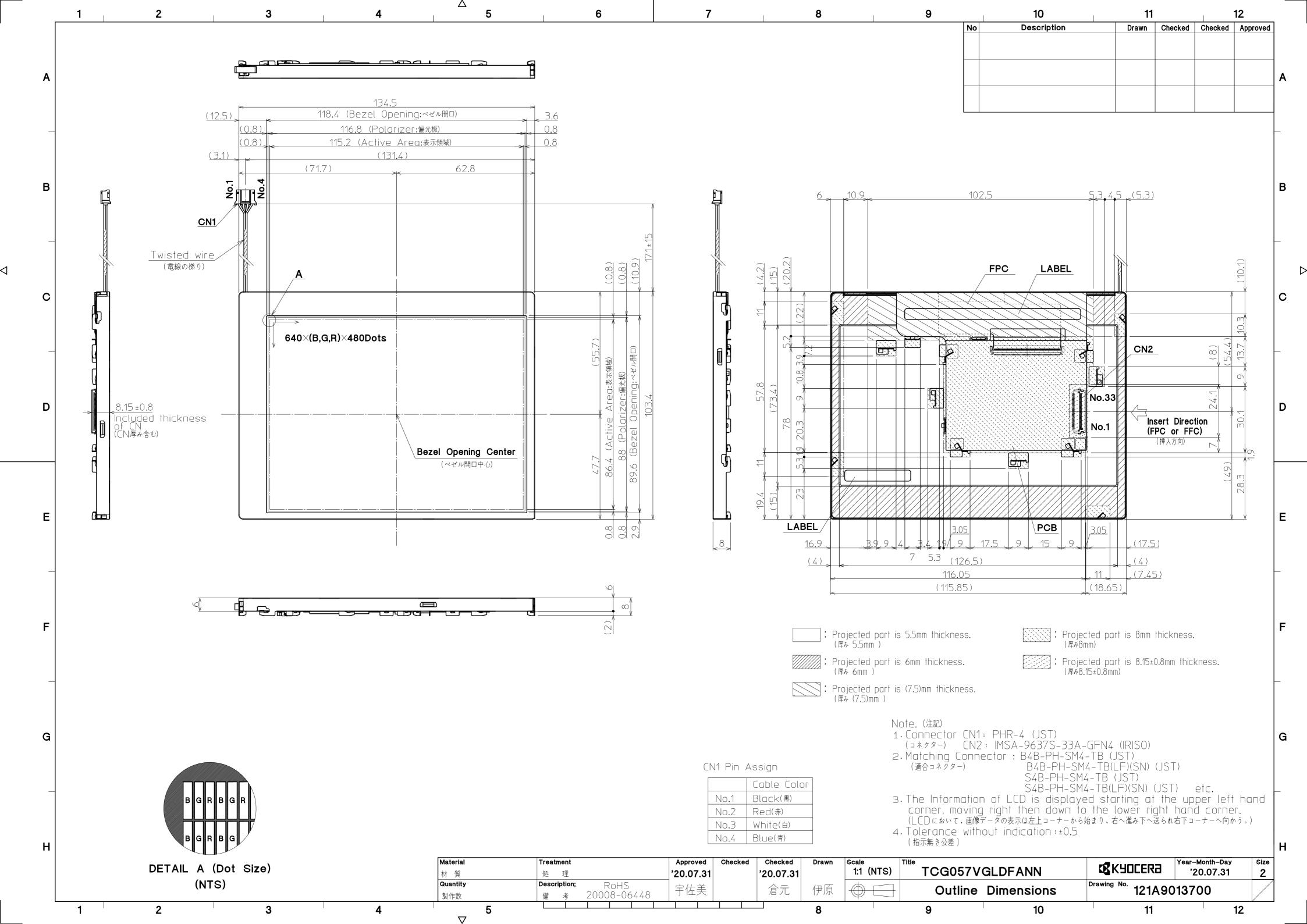
1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

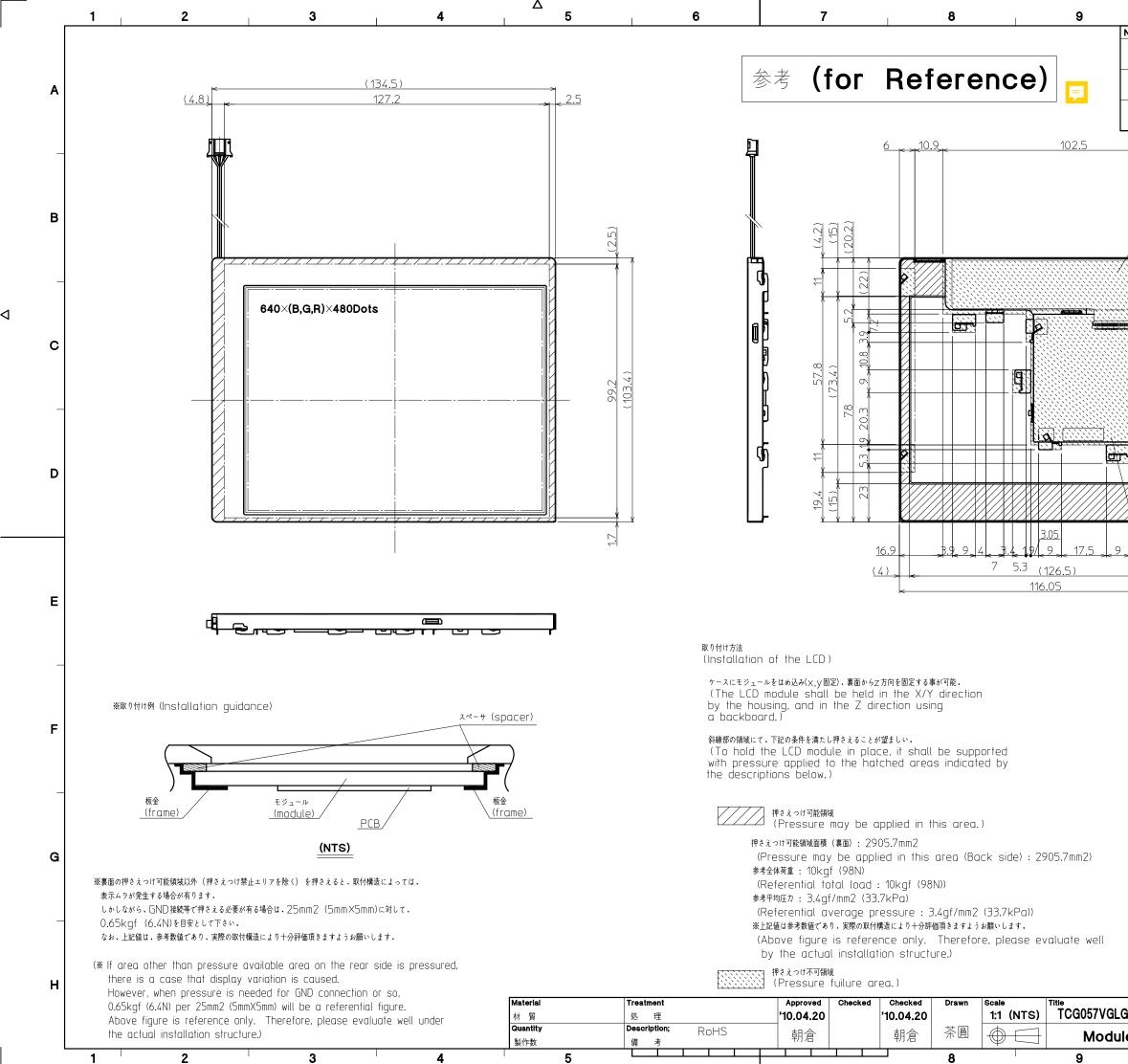
2) The LCD is tested in circumstances in which there is no condensation.

3) The reliability test is not an out-going inspection.

 The result of the reliability test is for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.

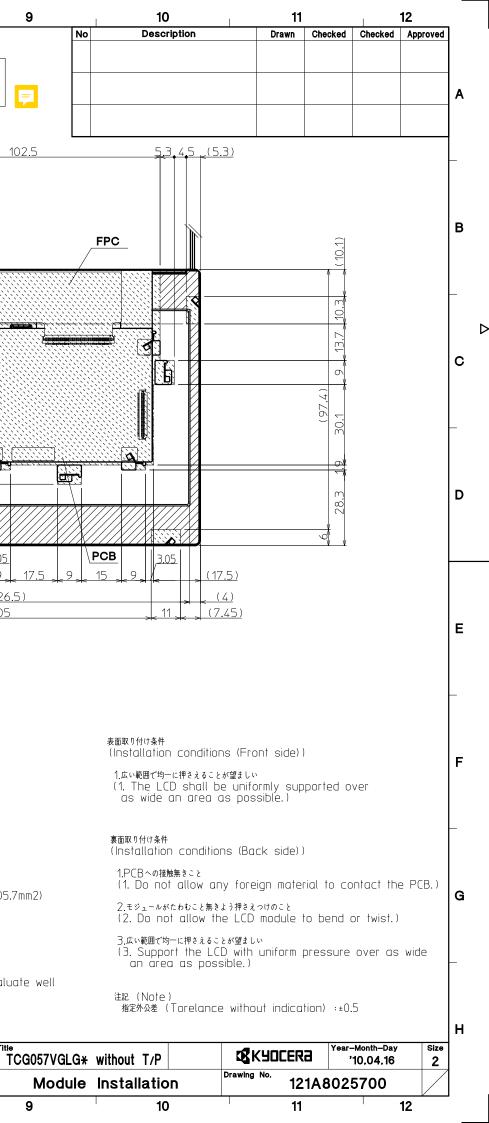






 ∇

5



Spec No.	TQ3C-8EAF0-E2YBD81-00
Date	April 13, 2023

KYOCERA INSPECTION STANDARD

TYPE : TCG057VGLDFANN-GN00

KYOCERA CORPORATION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 13, 2023	K. Komurasaki	I. Kawajiri	A. Iwasaki	Y. Aritsubo	M. Aoyama



		Γ	Spec No.		Part No.		Pa
			TQ3C-8EAF0-E2		TCG057VGLDFA	NN-GN00	
			evision r				
Date		Designed by : Engineering dept.			Confirmed by		
		Prepared	Checked	Approved	Checked	Approve	ed
Rev.No.	Date	Page		Descrip	tions		
				1			
		1 1					



Page 1

Visuals specification

1) Note

			Note				
General			efined within this inspection standard shall be reviewed by candard shall be determined by mutual consent.				
			nage quality shall be applied to any defect within the				
	active area and shall not be applicable to outside of the area.						
		tion conditions					
	Lumin		: 500 Lux min.				
		tion distance	: 300 mm.				
		erature	$:25 \pm 5^{\circ}$ C				
	Direct		: Directly above				
Definition of	Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the				
inspection		8	LCD, even when all "Black" data sent to the screen.				
item			Inspection tool: 5% Transparency neutral density filter.				
			Count dot: If the dot is visible through the filter.				
			Don't count dot: If the dot is not visible through the				
			filter.				
			RGBRGBRGB				
			R G B R G B R G B				
		Black dot defect	The dot is constantly "off" when power applied to the				
			LCD, even when all "White" data sent to the screen.				
			Similar size compared to bright dot.				
		White dot	Pixel works electrically, however, circular/foreign				
		(Circular/foreign particle)	particle makes dot appear to be "on" even when all				
			"Black" data is sent to the screen.				
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot				
			defects or black dot defects.				
	External	Bubble, Scratch, Foreign	Visible operating (all pixels "Black" or "White") and non				
	inspection	particle	operating.				
		(Polarizer, Cell, Backlight)					
		Appearance inspection	Does not satisfy the value at the spec.				
	Definition	Definition of circle	e size Definition of linear size				
	of size	<u>/</u>					
		A"Y					
			<mark>⊨ ⊢</mark> √				
		¥					
		a: major axis, b: min d = (a + b)/2	nor axis				
		d = (a + b) / 2					



Spec No. TQ3C-8EAF0-E2YBD81-00

Page 2

2) Standard

2) Standa Classif	fication	Inspect	tion item		Judgement	standar	d	
Defect	Single	Bright dot	defect	Acceptable number		:4	:4	
(in LCD	dot			Bright dot spacing		: 5 mm	or more	
glass)		Black dot	defect	Acceptable number		: 5		
				Black dot spacing		: 5 mm	or more	
	Adjacent dot	2 dots	Bright dot defect	Acceptable number	-		:2	
			Black dot defect	Acceptable number				
		3 or more	dots	Acceptable number		:0		
	Total dot	defects		Acceptable number		: 5 Max	x	
	Others	White dot,	Dark dot	*				
		(Circle)		Size (mm	ı)	Ac	ceptable number	
		(011010)		d ≦			(Neglected)	
				0.2 < d \leq	0.4		5	
				$0.4 < d \leq$	0.5		3	
				0.5< m d			0	
Futomal	inspection	Polarizer ((Constab)					
		Folarizer	Scratch)	Width (mm)	Length (1	mm)	Acceptable number	
(Defect on				$W \leq 0.1$			(Neglected)	
Polarizer				I <		≤ 5.0 (Neglected)		
between F				$0.1 < W \leq 0.3$	5.0 < L		0	
and LCD	glass)			0.3 < W			0	
		Polarizer (Bubble)						
				Size (mm)		Ac	ceptable number	
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \leq$			5	
				$0.3 < d \leq$	0.5		3	
				$0.5 < \mathrm{d}$			0	
		Foreign pa	article					
		(Circular shape)		Size (mm)		Acceptable number		
				d \leq 0.2		(Neglected)		
				$0.2 < d \leq 0.4$		5		
				$0.4 < d \leqq 0.5$		3		
				0.5< m d	0.5< m d		0	
		Foreign pa		Width (mm)	Length	(mm)	Acceptable number	
		(Linear sh	ape)	$W \leq 0.03$		(IIIII)	(Neglected)	
		Scratch		$W \ge 0.05$		< 20		
				$0.03 < W \leq 0.1$	$\frac{1}{2.0 < L}$	≤ 2.0	(Neglected) 3	
				$0.00 \times W = 0.1$	$\frac{2.0 < L}{4.0 < L}$	<u>⇒</u> 4.0	0	
					4.0 < L		÷	
				$0.1 < \mathrm{W}$	-		(According to	
							circular shape)	
		Color varia	ation	Not to be significantl	v visihle			
		(Mura)		Consultation shall be		0001117		
		(minia)		Consultation shall be	neiu as nece	ooary.		



Document No.	TQ3C-8EAF0-E3YBD81-00
Date	April 13, 2023

KYOCERA PACKAGING STANDARD

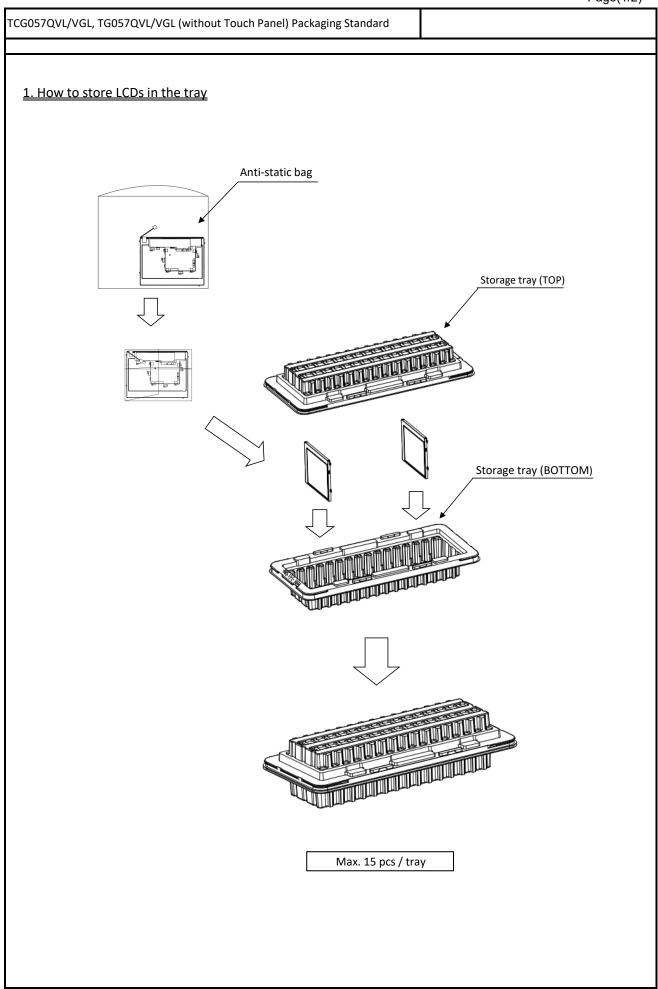
TYPE : TCG057VGLDFANN-GN00

KYOCERA CORPORATION

Original	Designed by :	Engineering de	Confirmed by : QA dept.		
Issue Date	Prepared	Checked	Approved	Checked	Approved
April 13, 2023	T. Onodera	I. Kawajiri	A. Iwasaki	Y. Aritsubo	M. Aoyama



			Document No.		Part No.	CLDEAN		Р
				0-E3YBD81-00	TCG057V	GLDFAN	IN-GINUU	
			evision			modby	· OA dont	
Date		Prepare		Engineering dept. Checked Approved		Confirmed by : QA dep Checked		
		Tiepare				JACU	Appiove	eu
RevNo.	Date	Page		Descriptions				





TCG057QVL/VGL, TG057QVL/VGL (without Touch Panel) Packaging Standard

