## **SPEC for Mass Production**

Spec No.	TQ3C-8EAF0-E1YAL88-00
Date	December 10, 2024

## TYPE: TCG057QVLCTANN-GN00

< 5.7 inch QVGA transmissive color TFT with LED backlight >

### **CONTENTS**

- 1. Application
- 2. Construction and outline
- 3. Mechanical specifications
- 4. Absolute maximum ratings
- 5. Electrical characteristics
- 6. Optical characteristics
- 7. Interface signals
- 8. Input timing characteristics
- 9. Backlight characteristics
- 10. Lot number identification
- 11. Warranty
- 12. Precautions for use
- 13. Reliability test data
- 14. Outline drawing



#### KYOCERA CORPORATION

This specification is subject to change without notice.

Consult Kyocera before ordering.

Original	Designed by: Engi	Designed by: Engineering dept.				
Issue Date	Prepared	Checked	Approved	Approved		
December 10, 2024	K. Komurasaki	T. Fukui	I. Kawajiri	T. Sawada		



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAL88-00	TCG057QVLCTANN-GN00	-

## Warning

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
- 2. Customer agrees to indemnity, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

## Caution

- 1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
- 2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAL88-00	TCG057QVLCTANN-GN00	-

# Revision record

Date  Designed by : Engineering dept.  Prepared Checked Approved Appro	ved
	-
Rev. No. Date Page Descriptions	

Spec No	).	Part No.	Page
TQ30	C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	1

## 1. Application

This document defines the specification of TCG057QVLCTANN-GN00. (RoHS Compliant)

## 2. Construction and outline

LCD : Transmissive color dot matrix type TFT

Backlight system : LED

Polarizer : Glare treatment

Additional circuit : Timing controller, Power supply (3.3V input)

(without constant current circuit for LED Backlight)

## 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	127.2 (W)× 100.4 (H) × 5.7 (D)	mm
Active area	115.2 (W) × 86.4 (H) (14.4cm / 5.7 inch (Diagonal))	mm
Dot format	$320 \times (B,G,R) (W) \times 240 (H)$	dot
Dot pitch	0.12 (W) × 0.36 (H)	mm
Base color 2)	Normally White	-
Mass	110	g

- 1) Projection not included. Please refer to outline for details.
- 2) Due to the characteristics of the LCD material, the color varies with environmental temperature.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	2

#### 4. Absolute maximum ratings

### 4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage for logic		$V_{ m DD}$	0	4.0	V
Input signal voltage	1)	$V_{\rm IN}$	-0.3	6.0	V
LED forward current	2)	IF	-	30	mA
Reversed voltage	2)	VR	-	5.0	V

- 1) Input signal: CK, R0~R5, G0~G5, B0~B5, H<sub>SYNC</sub>, V<sub>SYNC</sub>, ENAB, R/L, U/D
- 2) For each "AN-CA"

#### 4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	Тор	-20	70	$^{\circ}\mathrm{C}$
Storage temperature	2)	$T_{\mathrm{STO}}$	-30	80	$^{\circ}\mathrm{C}$
Operating humidity	3)	Нор	10	4)	%RH
Storage humidity	3)	Hsto	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Ta (Ambient Temperature) = -30°C<48h, Ta = 80°C<168h

  Store LCD at normal temperature/humidity. Keep them free from vibration and shock.

  An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

  (Please refer to "Precautions for Use" for details.)
- 1) Non-condensing
- 2) Ta  $\leq 40$ °C, 85%RH Max.

Ta >40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10∼55 Hz	Acceleration value
Vibration width	0.15mm	$(0.3\sim 9 \text{ m/s}^2)$
Interval	10-55-10	Hz 1 minute

2 hours in each direction X, Y, Z (6 hours total)

 $\hbox{EIAJ ED-}2531$ 

6) Acceleration: 490 m/s $^2$ , Pulse width: 11 ms

3 times in each direction:  $\pm X$ ,  $\pm Y$ ,  $\pm Z$ 

**EIAJ ED-2531** 



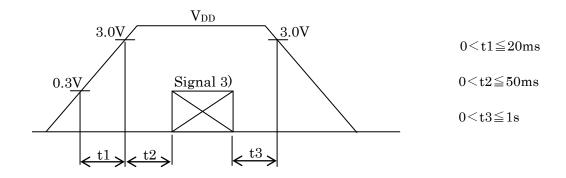
Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	3

#### 5. Electrical characteristics

 $Ta = -20 \sim 70$ °C

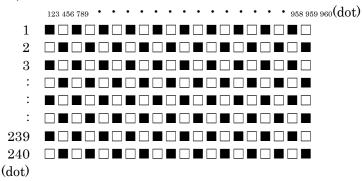
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage for logic 1)	$V_{ m DD}$	-	3.0	3.3	3.6	V
Current consumption for logic	${ m I}_{ m DD}$	2)	-	60	80	mA
Permissive input ripple voltage	$V_{\mathrm{RP}}$	-		-	100	mVp-p
	$ m V_{IL}$	"Low" level	0	-	$0.3 \mathrm{V}_\mathrm{DD}$	V
Input signal voltage 3)	$V_{\mathrm{IH}}$	"High" level	$0.7 V_{\mathrm{DD}}$	-	$V_{\mathrm{DD}}$	V

## 1) V<sub>DD</sub>-turn-on conditions



2) Display pattern:

$$V_{DD} = 3.3V, Ta = 25^{\circ}C$$



3) Input signal : CK, R0 $\sim$ R5, G0 $\sim$ G5, B0 $\sim$ B5, Hsync, Vsync, ENAB, R/L, U/D



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-0	0 TCG057QVLCTANN-GN00	4

# 6. Optical characteristics

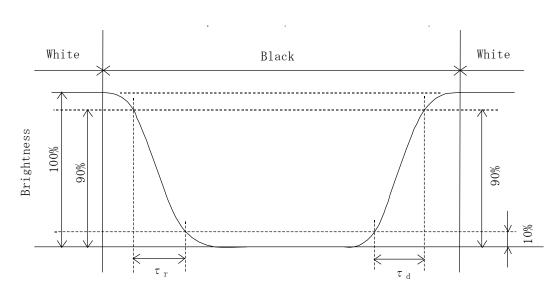
Measuring spot =  $\phi$  6.0mm, Ta = 25°C

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	
D	Rise	τr	$\theta = \phi = 0$ °	-	10	-	ms	
Response time	Response time Down td		$\theta = \phi = 0$ °	-	25	-	ms	
Viewing angle	rango	$\theta$ upper		-	80	-	1	
Viewing angle View direction	_	$\theta$ lower	CD > F	-	80	-	$\deg$ .	
: 12 o'clo		$\phi$ left	CR≧5	-	80	-	1	
(Gray inversion)		φ right		-	80	-	deg.	
Contrast ratio		CR	$\theta = \phi = 0$ °	300	500	-	-	
Brightness		L	IF=15mA/Line	210	300	-	cd/m²	
	Red	x	$\theta = \phi = 0$ °	0.57	0.62	0.67		
		У		0.32	0.37	0.42		
	G	X	$\theta = \phi = 0^{\circ}$	0.28	0.33	0.38		
Chromaticity	Green	У	$\theta - \phi - 0$	0.54	0.59	0.64		
coordinates	DI	x	0 - 1 -00	0.09	0.14	0.19	-	
	Blue	У	$\theta = \phi = 0^{\circ}$	0.04	0.09	0.14		
	3371 :4	X	0 - 1 -00	0.27	0.32	0.37		
	White	у	$\theta = \phi = 0^{\circ}$	0.29	0.34	0.39		

### 6-1. Definition of contrast ratio

$$CR(Contrast ratio) = \frac{Brightness with all pixels "White"}{Brightness with all pixels "Black"}$$

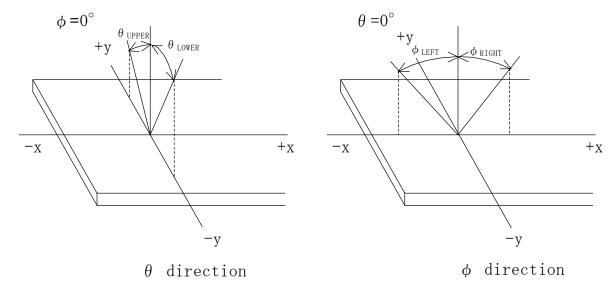
## 6-2. Definition of response time



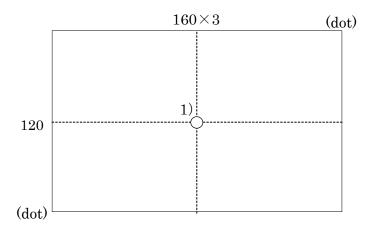


Spec No.	Part No.	Page	•
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	5	

# 6-3. Definition of viewing angle



## 6-4. Brightness measuring point



- 1) Rating is defined as the white brightness at center of display screen.
- 2) Measured 5 minutes after the LED is powered on. (Ta = 25°C)

Ī	Spec No.	Part No.	Page
	TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	6

# 7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	CK	Clock signal for sampling each data signal	I	
3	$H_{\mathrm{SYNC}}$	Horizontal synchronous signal (negative)	I	
4	$V_{\mathrm{SYNC}}$	Vertical synchronous signal (negative)	I	
5	GND	GND	-	
6	R0	RED data signal (LSB)	I	
7	R1	RED data signal	I	
8	R2	RED data signal	I	
9	R3	RED data signal	I	
10	R4	RED data signal	I	
11	R5	RED data signal (MSB)	I	
12	GND	GND	-	
13	G0	GREEN data signal (LSB)	I	
14	G1	GREEN data signal	I	
15	G2	GREEN data signal	I	
16	G3	GREEN data signal	I	
17	G4	GREEN data signal	I	
18	G5	GREEN data signal (MSB)	I	
19	GND	GND	-	
20	В0	BLUE data signal (LSB)	I	
21	B1	BLUE data signal	I	
22	B2	BLUE data signal	I	
23	В3	BLUE data signal	I	
24	B4	BLUE data signal	I	
25	B5	BLUE data signal (MSB)	I	
26	GND	GND	-	
27	ENAB	Signal to settle the horizontal display position (positive)	I	1)
28	$V_{ m DD}$	3.3V power supply	-	
29	$V_{ m DD}$	3.3V power supply	-	
30	R/L	Horizontal display mode select signal L: Normal , H: Left / Right reverse mode	I	2)
31	U/D	Vertical display mode select signal H: Normal , L: Up / Down reverse mode	I	
32	NC	No connect	-	
33	CA1	Cathode 1	-	
34	CA2	Cathode 2	-	
35	CA3	Cathode 3		
36	NC	No connect	-	
37	AN1	Anode 1	-	
38	AN2	Anode 2	-	
39	AN3	Anode 3	-	
40	NC	No connect	-	

LCD connector (CN1) : IMSA-9637S-40C-GFN4 (IRISO)

Recommended matching FFC or FPC : 0.5mm pitch



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	7

1) The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined.

Don't keep ENAB "High" during operation.

2)



R/L = LU/D = H



R/L = HU/D = H



R/L = LU/D = L



R/L = HU/D = L

Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	8

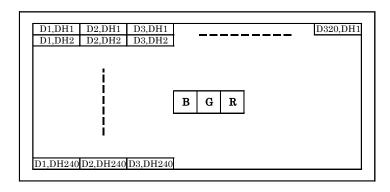
## 8. Input timing characteristics

### 8-1. Timing characteristics

	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clash sime al	Frequency	1/Tc	_	6.3	7.0	MHz	1) 2)
Clock signal	Duty ratio	Tch/Tc	40	50	60	%	
Data signal	Set up time	Tds	12	_	_	ns	
Data signal	Hold time	Tdh	12	_	_	ns	
	Cools	TH	50.0	63.6	_	$\mu$ s	
	Cycle	lП	360	400	450	clock	
Horizontal sync. signal	Pulse width	ТНр	5	30	_	clock	
Signar	Set up time	THs	12	_	_	ns	
	Hold time	THh	12	_	_	ns	
	Cycle	TV	251	262	280	line	
Vertical sync.	Pulse width	TVp	1	3	5	line	
signal	Set up time	TVs	2	_	_	clock	
	Hold time	TVh	12	_	_	ns	
	Pulse width	TEp		320		clock	
Enable signal (ENAB)	Set up time	TEs	12	_	_	ns	
(LIVID)	Hold time	TEh	12	_	_	ns	
H <sub>SYNC</sub> - Enable signal phase difference		THE	36	68	88	clock	
Vertical sync. signal start position		TVE	2	18	38	line	
Horizontal displa	Horizontal display period		320			clock	
Vertical display	period	TVd		240		line	

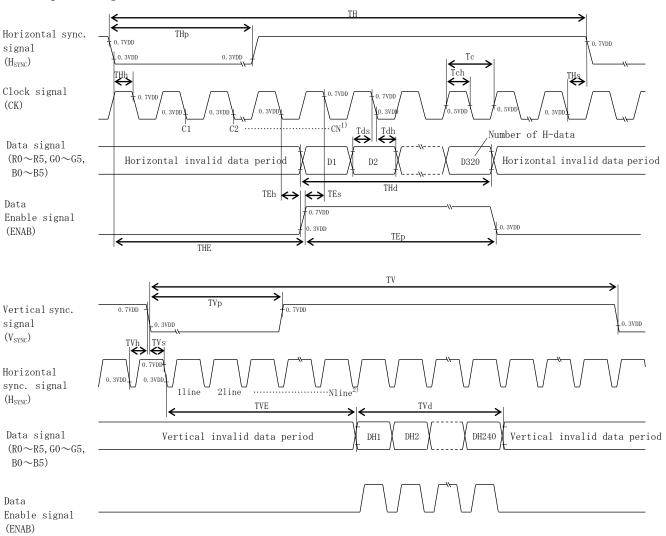
- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur. Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.
- 3) When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock) as shown in 8-3.
- 4) When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line) as shown in 8-3.

### 8-2. Input data signals and display position on the screen





## 8-3. Input timing characteristics



- 1) When ENAB is fixed at "Low", the horizontal display starts from the data of C68 (clock).
- 2) When ENAB is fixed at "Low", the vertical sync. signal start position is 18 (line).

S	Spec No.	Part No.	Page
	TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	10

## 9. Backlight characteristics

Item		Symbol	Min.	Тур.	Max.	Unit	Note			
Forward current	1)	IF	1	15	1	mA	Ta=-20~70°C			
			-	22.1	25.0	V	IF=15mA, Ta=-20°C			
Forward voltage	1)	VF	-	21.7	24.5	V	IF=15mA, Ta=25℃			
				ļ				-	21.3	24.1
Operating life time	2) 3)	Т	-	40,000	-	h	IF=15mA, Temp. = $25^{\circ}$ C			

- 1) For each "AN-CA"
- 2) When brightness decrease 50% of initial brightness

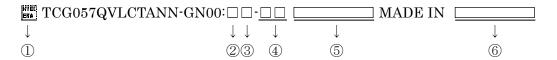
  The average life of LED will decrease when the LCD is operating at higher temperatures.
- 3) Life time is estimated data. (Condition: IF=15mA, Temp. = $25^{\circ}$ C in chamber).
- 4) An input current below 5.0mA may reduce the brightness uniformity of the LED backlight. This is because the amount of light from each LED chip is different. Therefore, please evaluate carefully before finalizing the input current.
- 5) LED formation: 7 series, 3 parallel



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	11

### 10. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.



No.① - No.⑥ above indicate

- ① Data matrix (For internal control purpose only)
- 2 Year code (The last digit of the year)
- ③ Month code
- 4 Day code
- 5 Version number (Max. 7 characters)
- 6 Country of origin

#### ③ Month code

Mon	th	Jan.	Feb.	Mar.	Apr.	May	Jun.
Cod	e	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

#### 11. Warranty

## 11-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

#### 11-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	12

#### 12. Precautions for use

#### 12-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed so that there is no pressure on the LSI chips.
- 3) The LCD shall be installed flat, without twisting or bending.
- 4) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

#### 12-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

### 12-3. LCD operation

1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

#### 12-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

#### 12-5. Usage

- 1) <u>DO NOT</u> store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD module because it will result in damage.
- 7) This Kyocera LCD module has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used
- 9) Liquid crystal may leak when the module is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.



Spec No.	Part No.	Page
TQ3C-8EAF0-E1YAN88-00	TCG057QVLCTANN-GN00	13

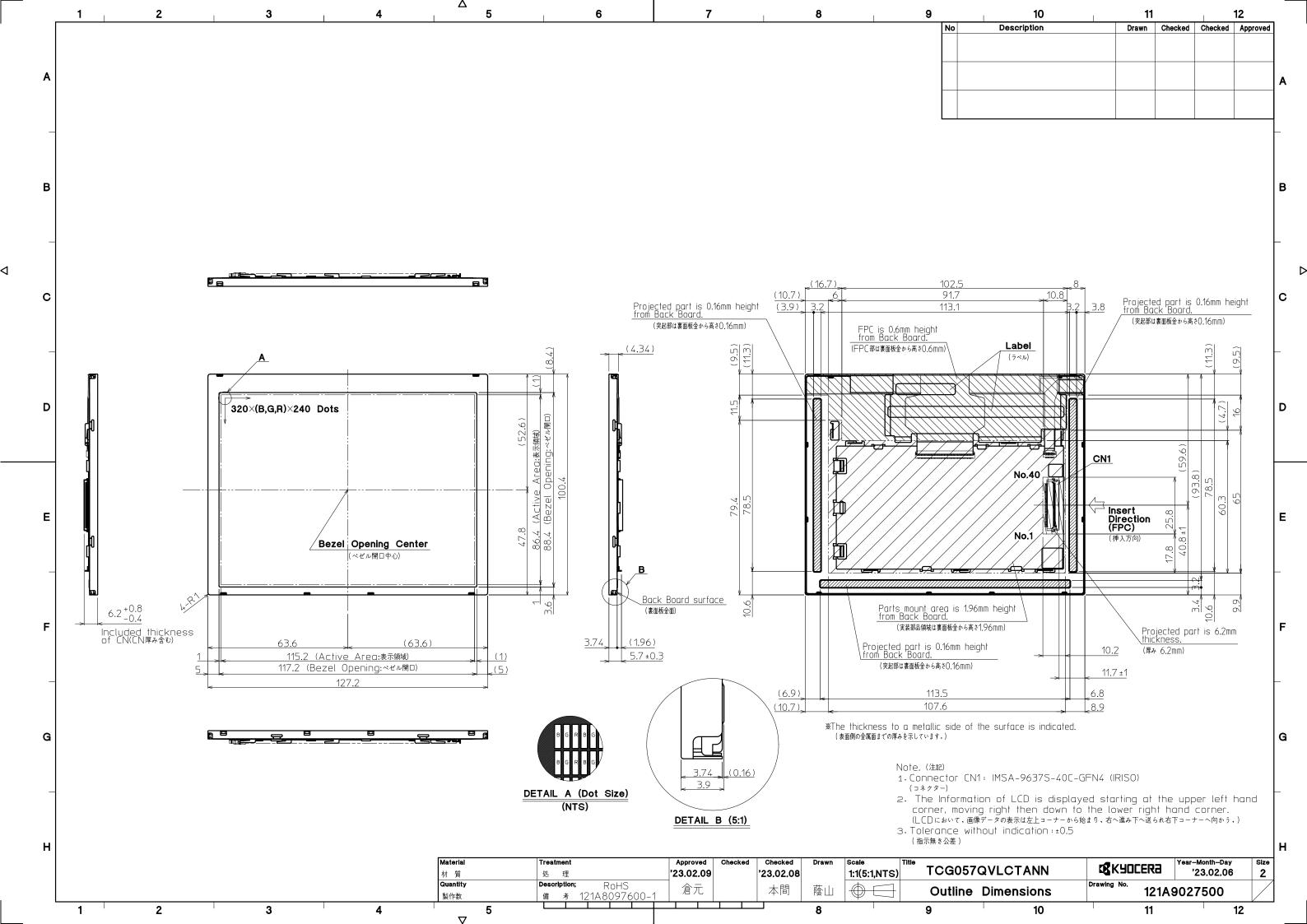
## 13. Reliability test data

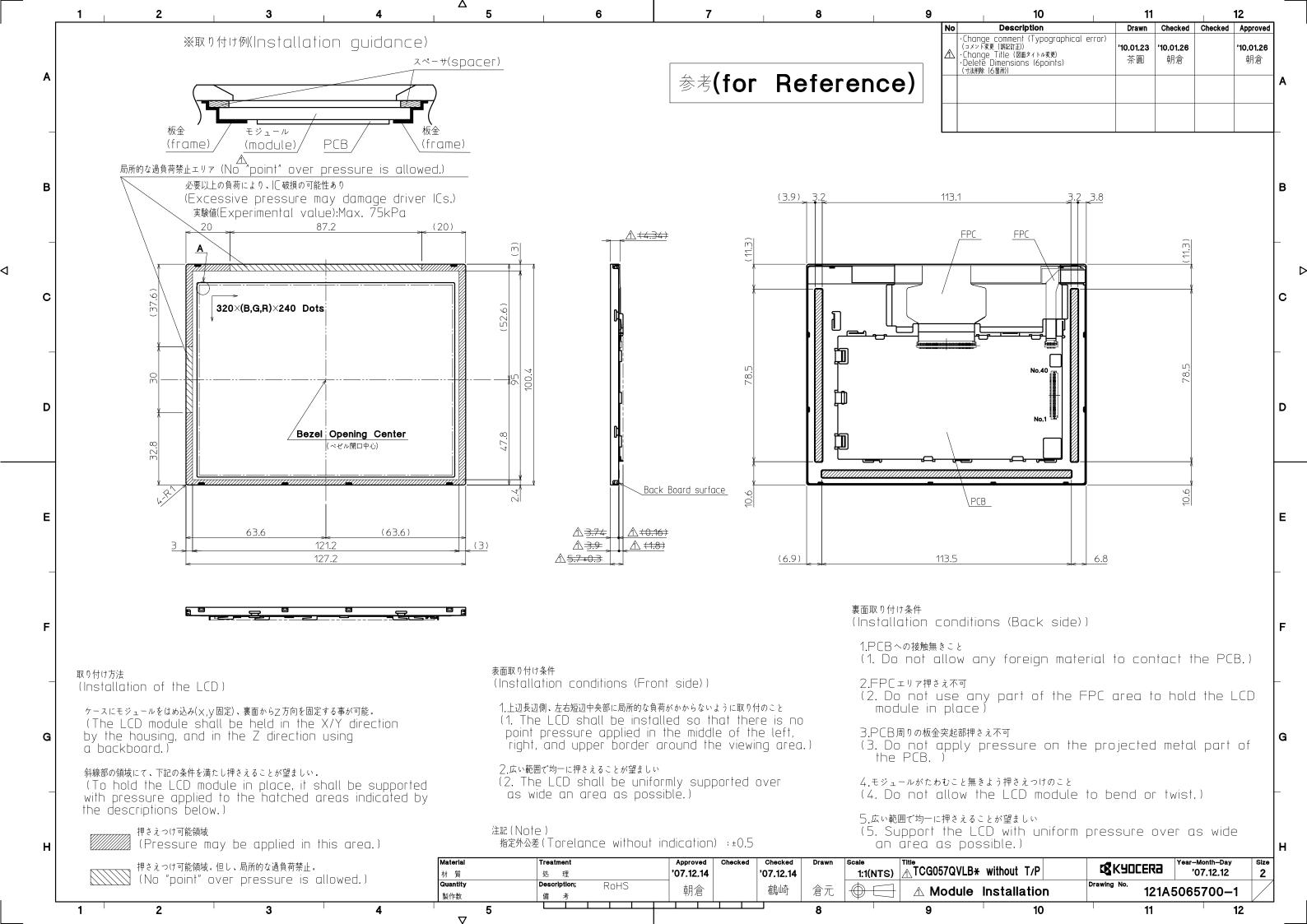
Test item	Test condition	Test time	Jud	gement
High temp. atmosphere	80°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Low temp. atmosphere	-30°C	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function Display quality Current consumption	: No defect : No defect : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function Display quality Current consumption	: No defect : No defect : No defect
High temp. operation	70°C	500h	Display function Display quality Current consumption	: No defect : No defect : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.

  The reliability test is conducted only to examine the LCD's capability.







Spec	No.	TQ3C-8EAF0-E2YAN88-00
Da	te	December 13, 2024

# KYOCERA INSPECTION STANDARD

TYPE: TCG057QVLCTANN-GN00

## KYOCERA CORPORATION

Original	Designed by: Engineering dept.			Confirmed by: QA dept.
Issue Date	Prepared	Checked	Approved	Approved
December 13, 2024	K. Komurasaki	T. Fukui	I. Kawajiri	T. Sawada



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAN88-00	TCG057QVLCTANN-GN00	-

# Revision record

Date		Designed by : Engineering dept.				Confirmed by : QA dept.
	Date	Prepared		Checked	Approved	Approved
Rev. No.	Date	Page		Des	scriptions	

Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAN88-00	TCG057QVLCTANN-GN00	1

# Visuals specification

		Note			
reviewed by Kyocera, and an additional standard shall be determined by mutual					
<ol> <li>This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area.</li> </ol>					
3. Inspec	tion conditions				
		: 500 Lux min.			
_		: 300 mm.			
		$:25~\pm~5^{\circ}\!\mathrm{C}$			
		: Directly above			
Dot defect	Bright dot defect	The dot is constantly "on" when power applied to the			
		LCD, even when all "Black" data sent to the screen.			
		Inspection tool: 5% Transparency neutral density filter.			
		Count dot: If the dot is visible through the filter			
		Don't count dot: If the dot is not visible through the filter.			
		R G B R G B R G B R G B R G B R G B R G B R G B R G B dot defect			
	Black dot defect	The dot is constantly "off" when power applied to the LCD, even when all "White" data sent to the screen.			
	Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.			
		R G B R G B R G B R G B R G B R G B R G B R G B R G B			
External	Bubble, Scratch,	Visible operating (all pixels "Black" or "White") and non			
inspection	Foreign particle	operating.			
	(Polarizer, Cell,				
	Backlight)				
	Appearance	Does not satisfy the value at the spec.			
	inspection				
Definition	Definition of	of circle size Definition of linear size			
of size		<b>^</b>			
	-	s, b: minor axis			
	External inspection	reviewed by Kyocera, and as consent.  2. This inspection standard ab the active area and shall no 3. Inspection conditions    Luminance    Inspection distance    Temperature    Direction  Dot defect  Black dot defect  Adjacent dot  External inspection  Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)  Appearance inspection  Definition of size  This inspection standard ab the active area and shall no 3. Inspection Standard ab the active area and shall no 4.  Black dot defect  Adjacent dot			



Spec No.	Part No.	Page
TQ3C-8EAF0-E2YAN88-00	TCG057QVLCTANN-GN00	2

## 2) Standard

2) Standard Classification		Inspection item		Judgement standard				
Defect	Single	-		Acceptable number : 4				
		Bright dot defect		_	_			
(in LCD dot		Black dot defect		Bright dot spacing : 5 mm or more  Acceptable number : 5				
glass)		DIACK GOT GETECT		•				
	A 1:	0.14		Black dot spacing : 5 mm or more				
	Adjacent dot	2 dots	Bright dot defect	Acceptable number : 2				
			Black dot defect	Acceptable number		: 3		
		3 or more dots		Acceptable number : 0				
	Total dot	Total dot defects		Acceptable number : 5 Max				
	Others	White dot, Dark dot						
		(Circle)		Size (mm)		Acceptable number		
				d ≤ 0.2		(Neglected)		
				$0.2 < d \le 0.4$		5		
					$0.4 < d \le 0.5$		3	
				0.5 < d			0	
External	inspection	Polarizer (	Scratch)					
(Defect on				Width (mm)	Length (m	m)	Acceptable number	
Polarizer or				W ≤ 0.1	_		(Neglected)	
between Polarizer				$0.1 < W \le 0.3$	$L \leq 5.0$		(Neglected)	
and LCD glass)					5.0 < L		0	
				0.3 < W	_		0	
		Polarizer (Bubble)						
				Size (mm)		Acceptable number		
				d ≤ 0.2		(Neglected)		
				$0.2 < d \le 0.3$		5		
				$0.3 < d \le 0.5$		3		
				0.5 < d		0		
		Foreign particle (Circular shape)		Size (mm)		Acceptable number		
				$d \leq 0.2$		(Neglected)		
				$0.2 < d \le 0.4$		(Neglecteu)		
				0.2 < d = 0.4 $0.4 < d \le 0.5$		3		
				0.5 < d		0		
							v	
			ırticle			-		
		(Linear shape) Scratch  Color variation		Width (mm)	Length (	mm)	Acceptable number	
				$W \leq 0.03$			(Neglected)	
				$0.03 < W \le 0.1$		≦ 2.0	(Neglected)	
					2.0 < L \( \)	≦ 4.0	3	
					4.0 < L		0	
				0.1 < W	_		(According to	
							circular shape)	
				Not visible through 5% ND filter.				
		(Mura)		Not visible through 5	70 ND Iliter.			
		(wiura)						

